

Figure 1

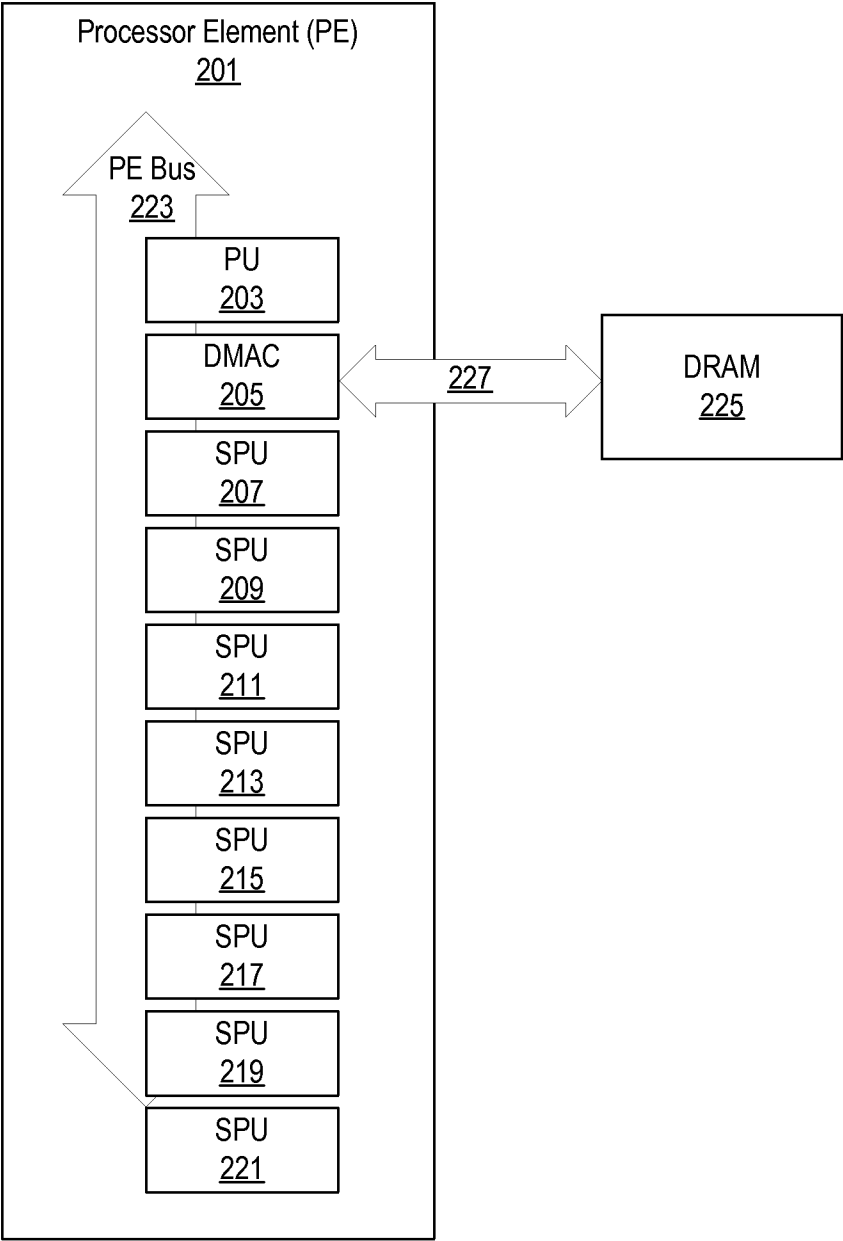


Figure 2

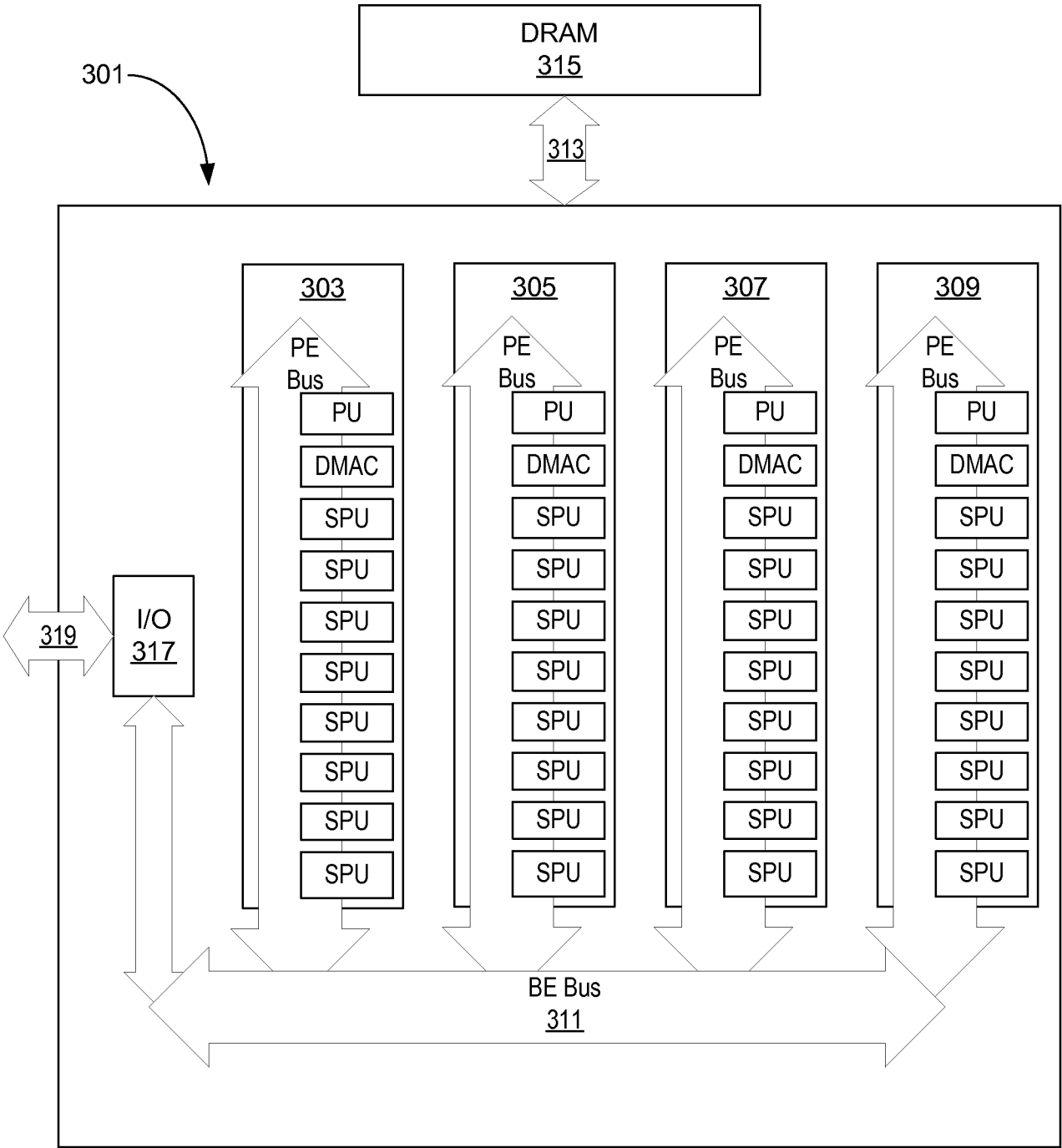


Figure 3

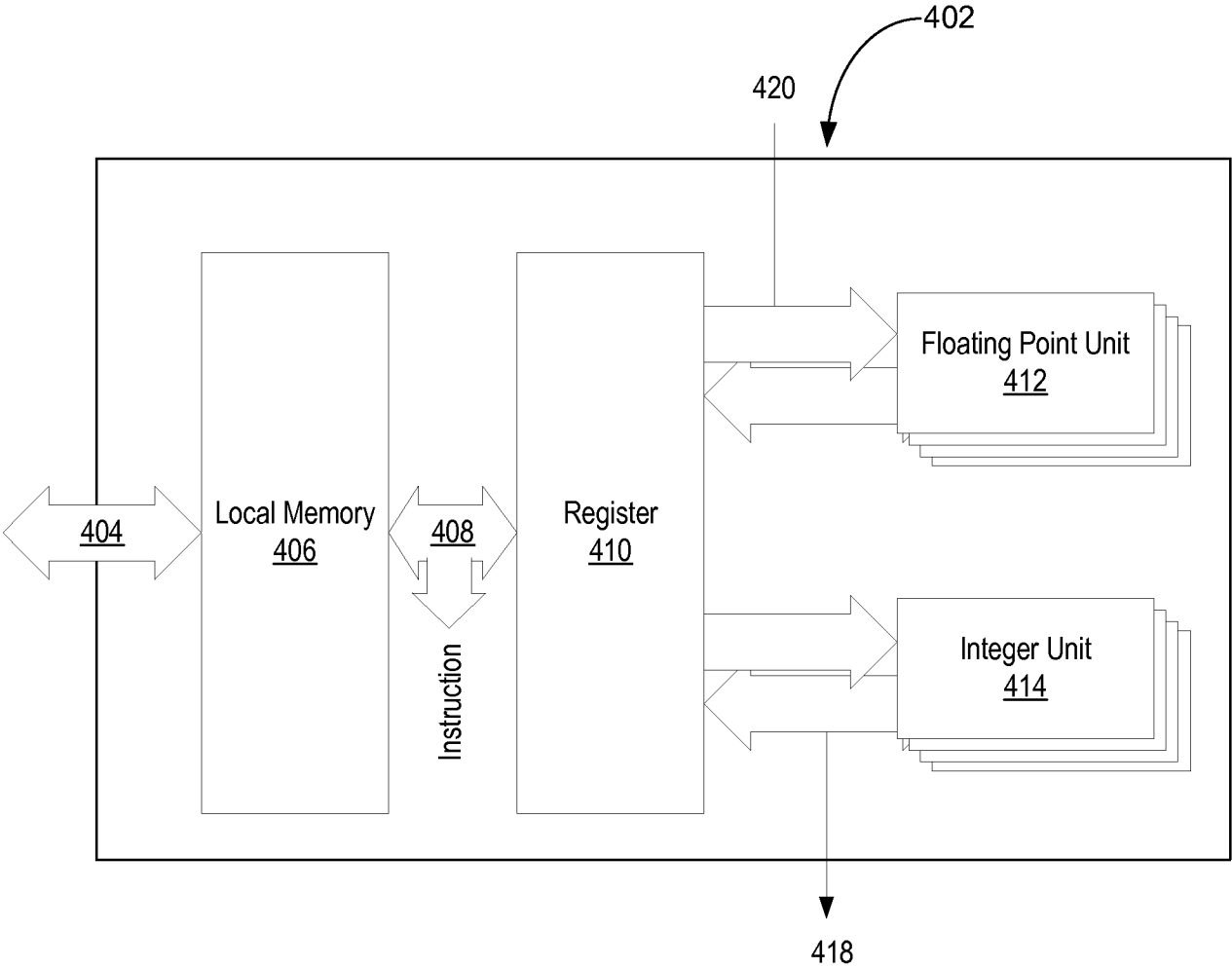


Figure 4

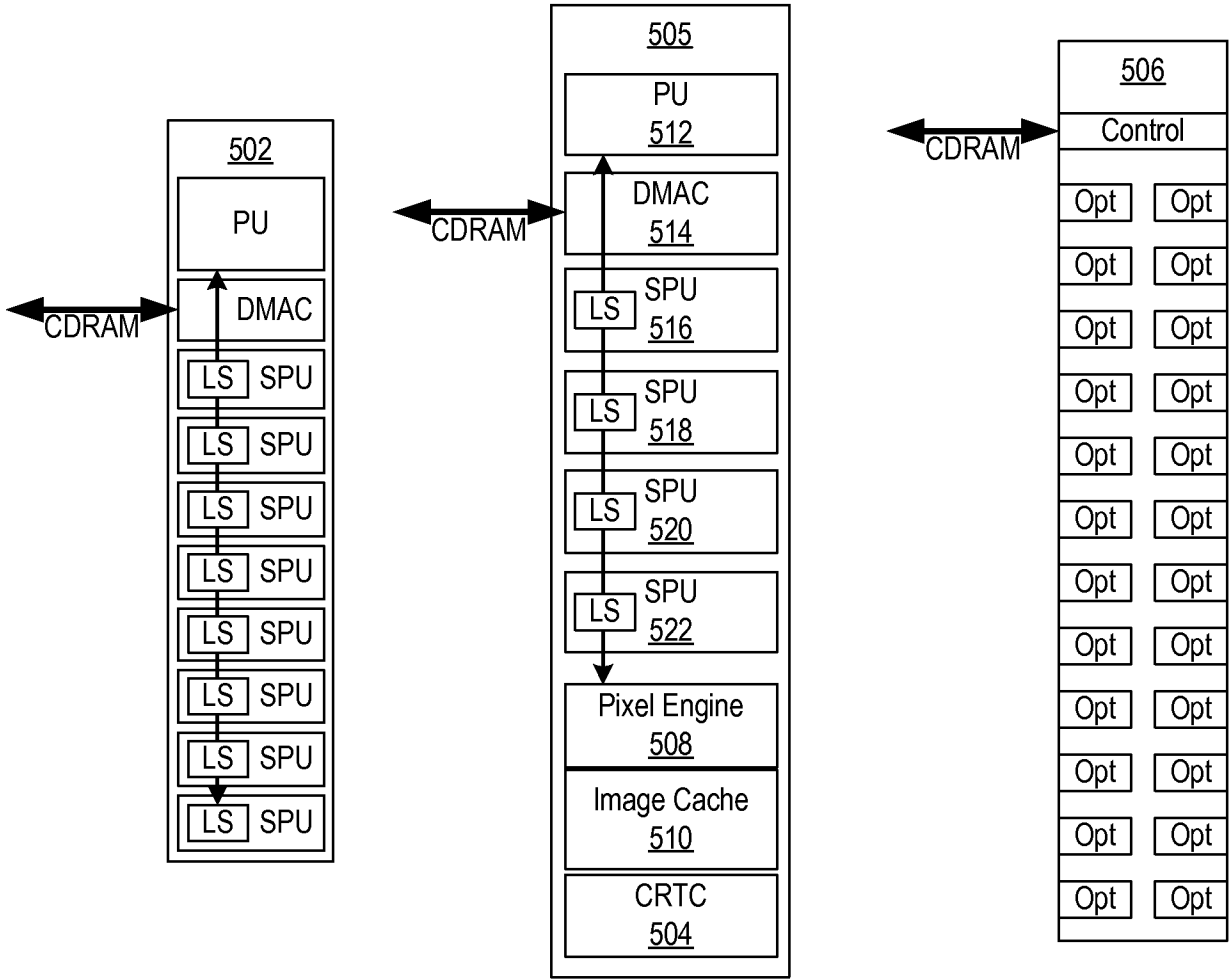


Figure 5

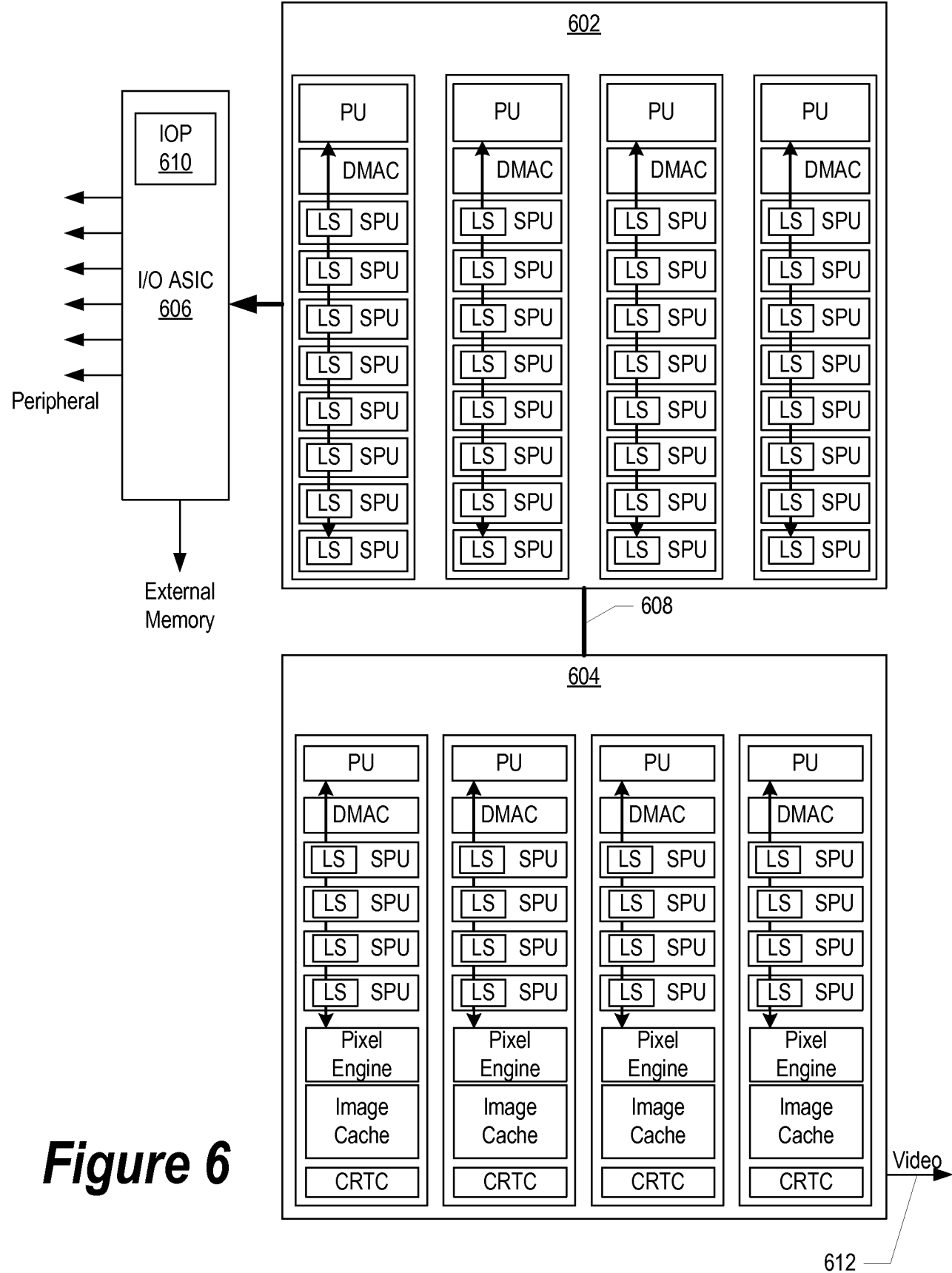


Figure 6

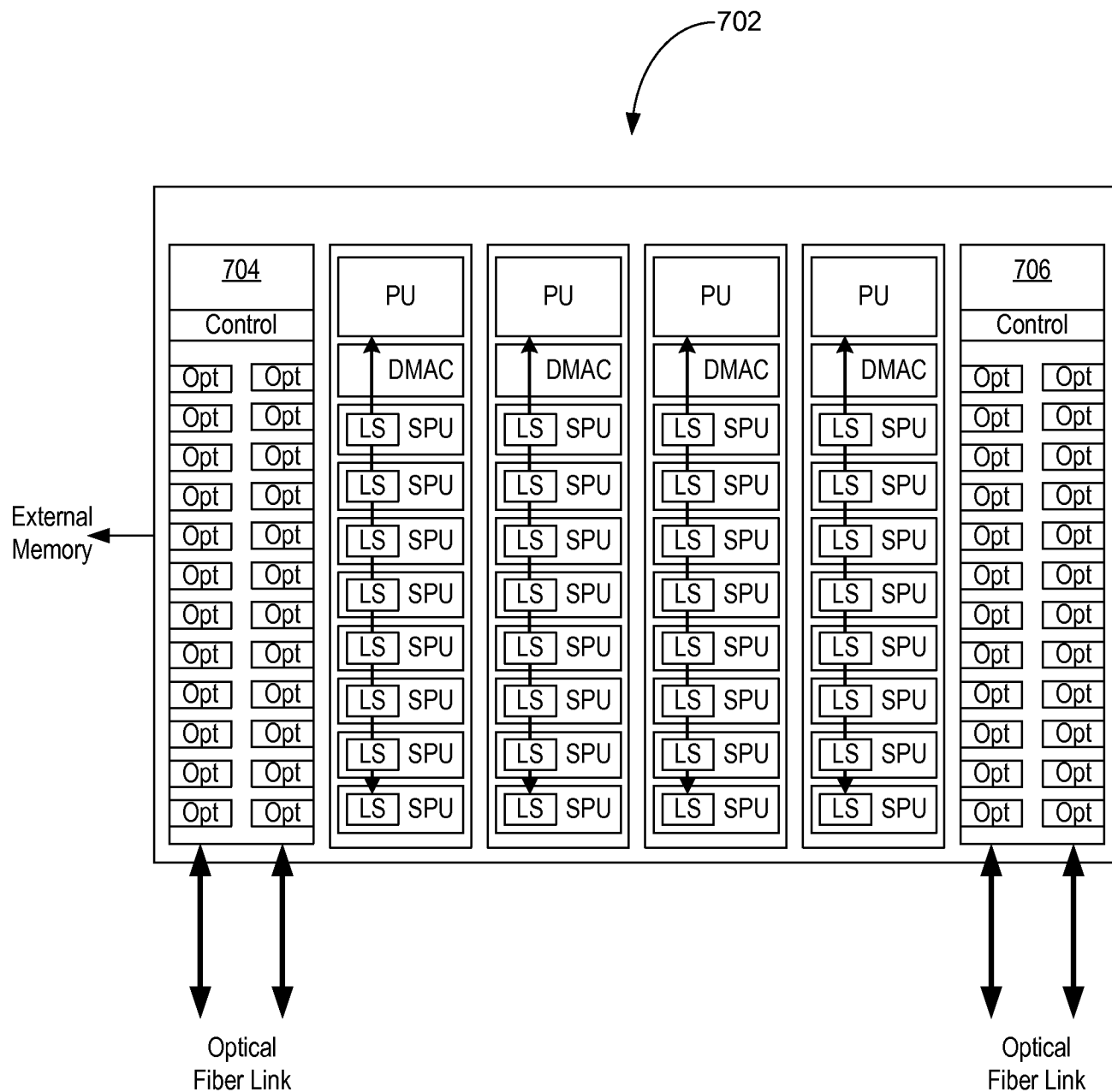


Figure 7

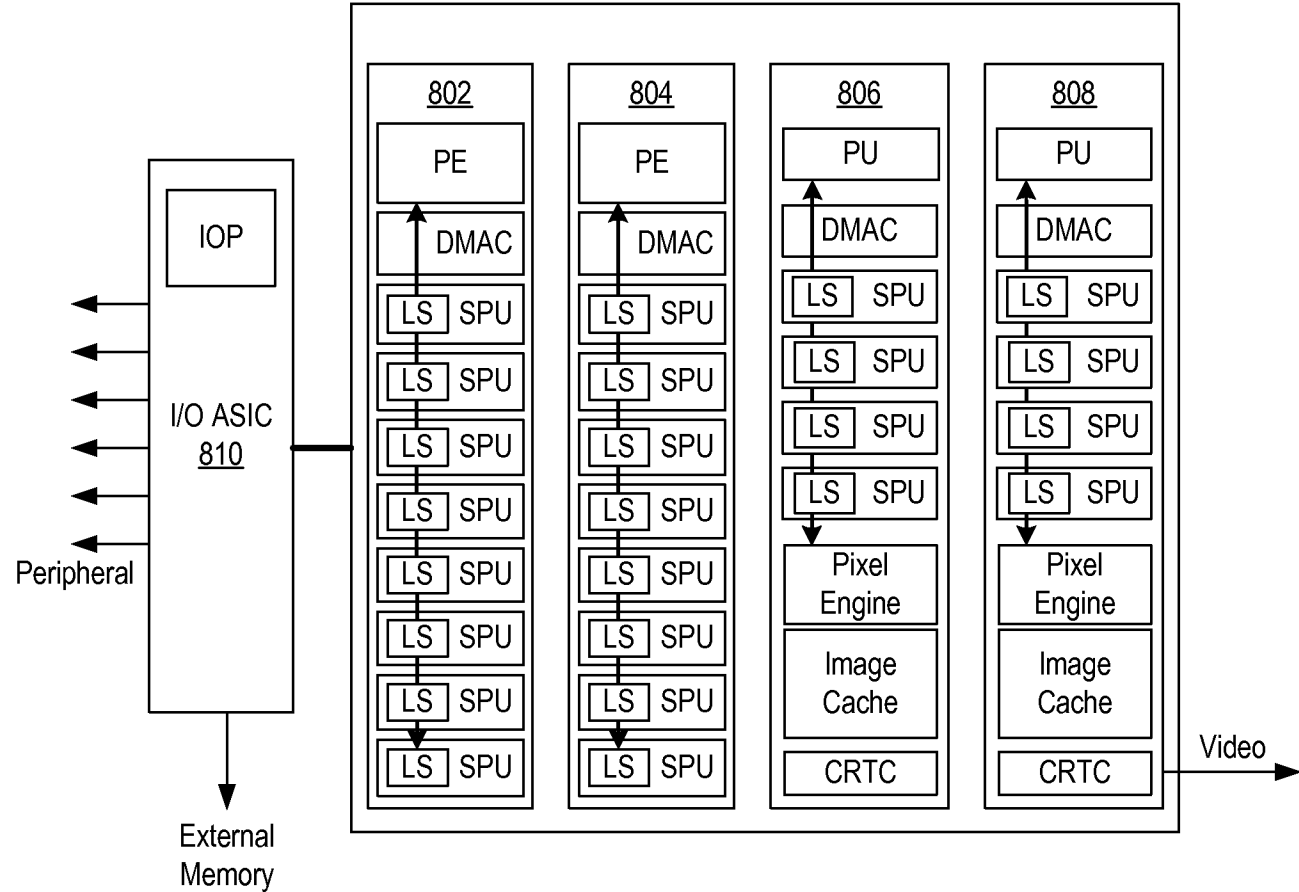


Figure 8

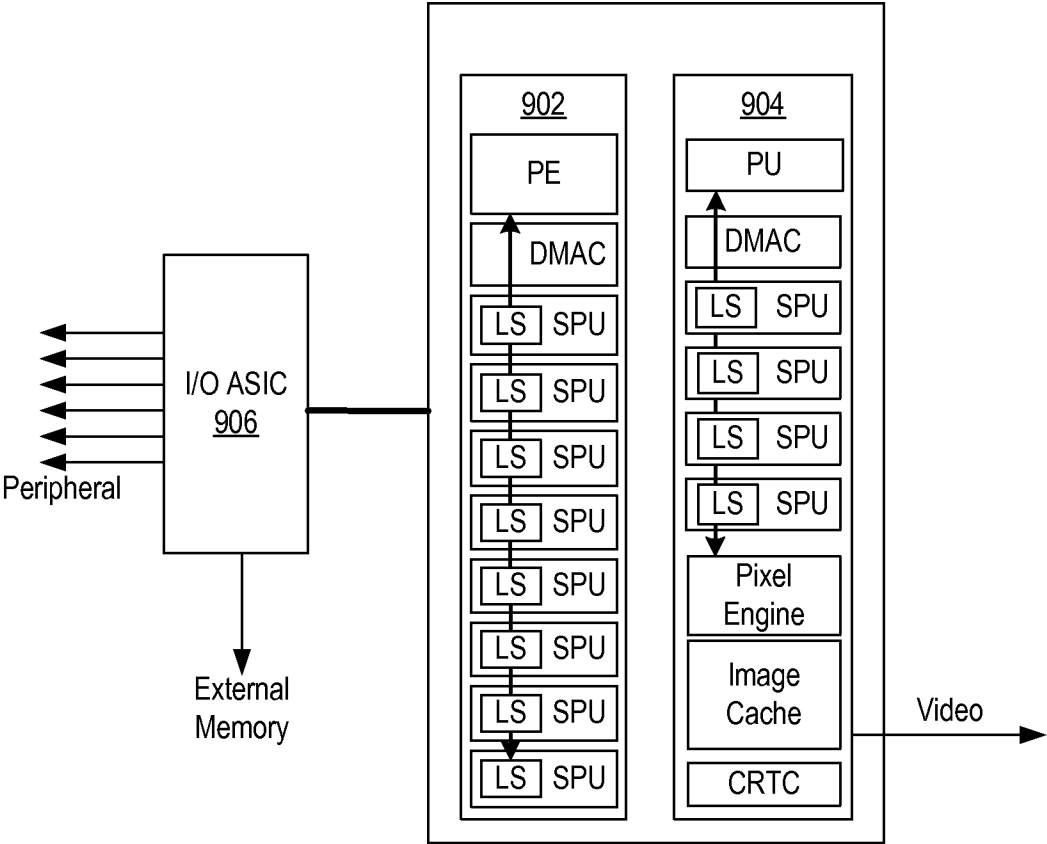


Figure 9

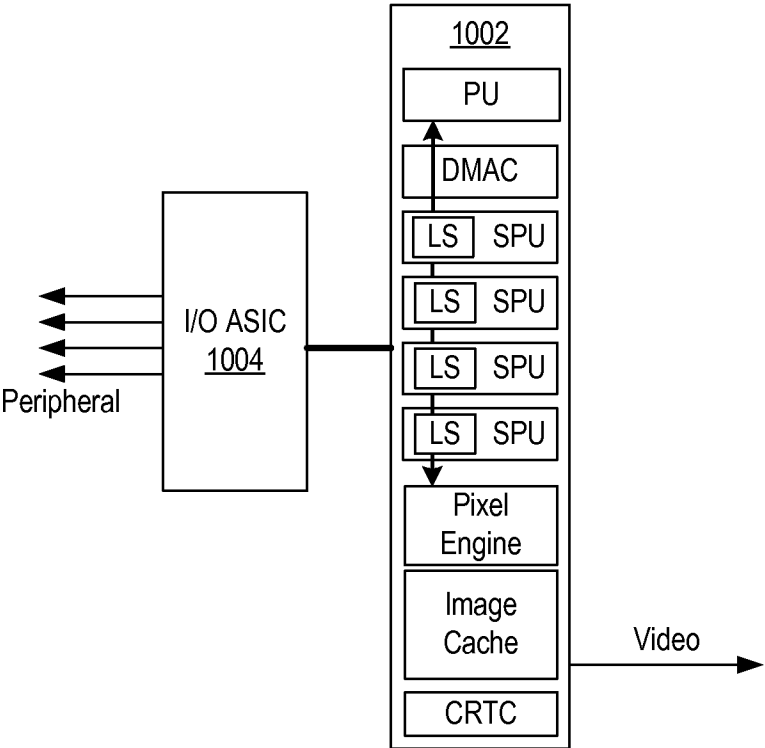


Figure 10

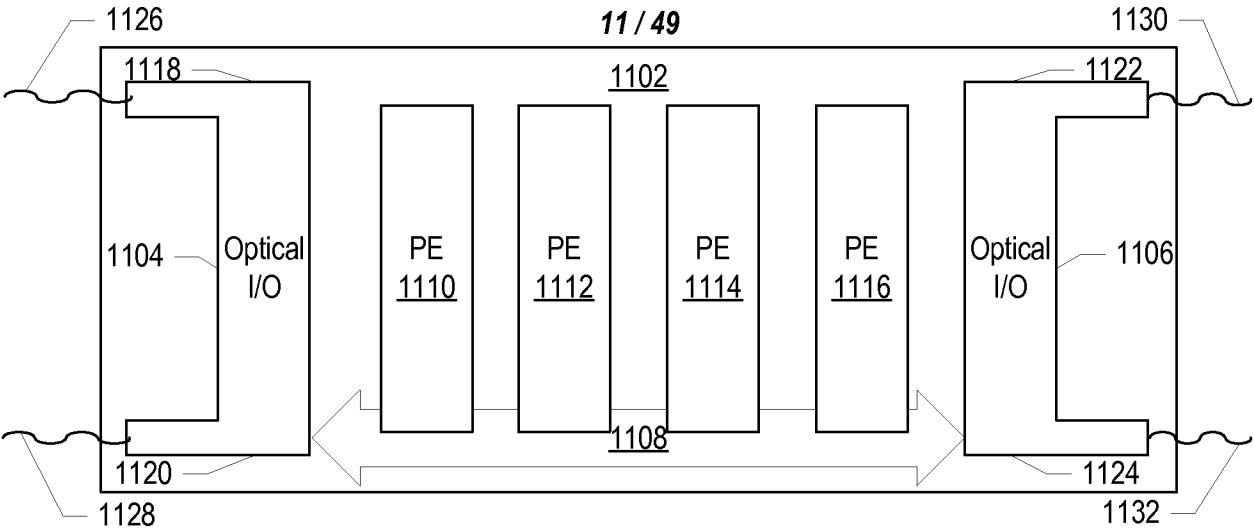


Figure 11A

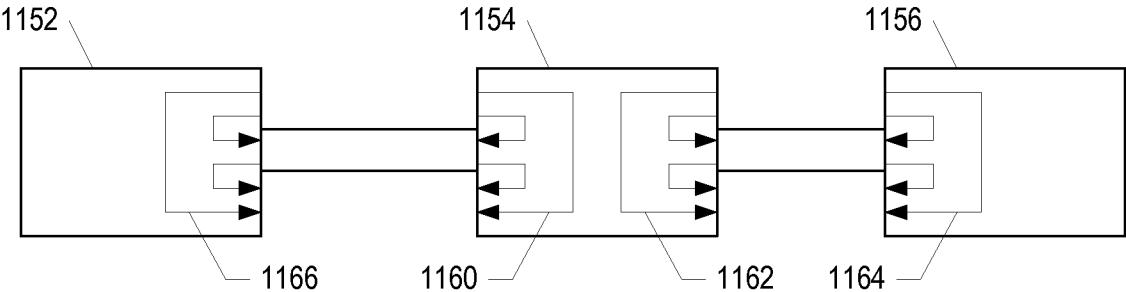


Figure 11B

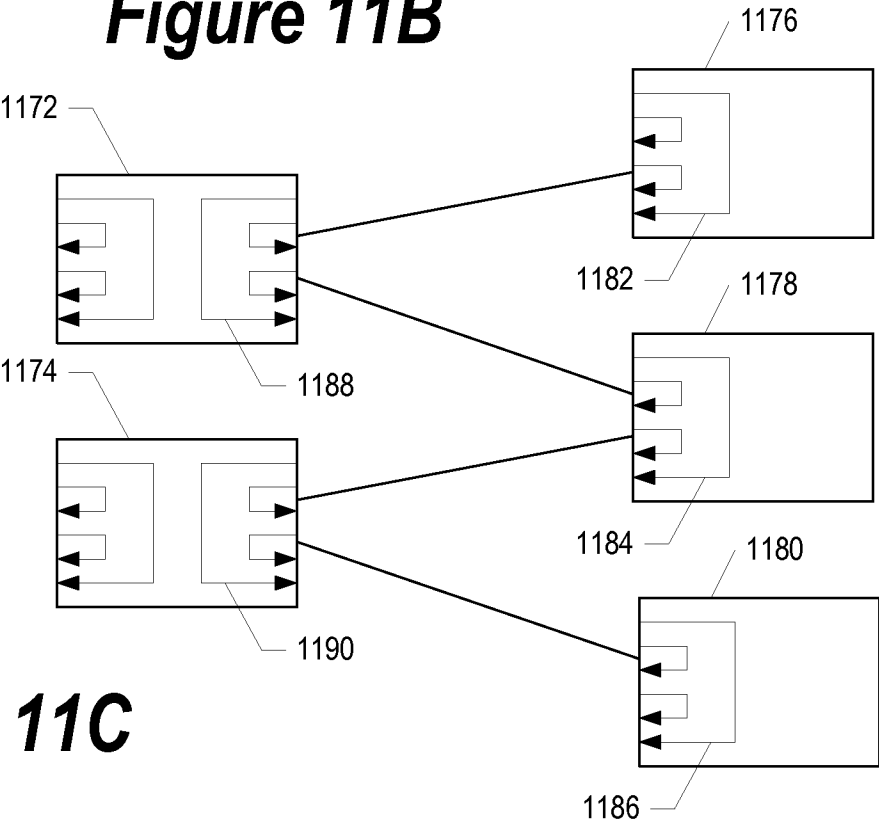


Figure 11C

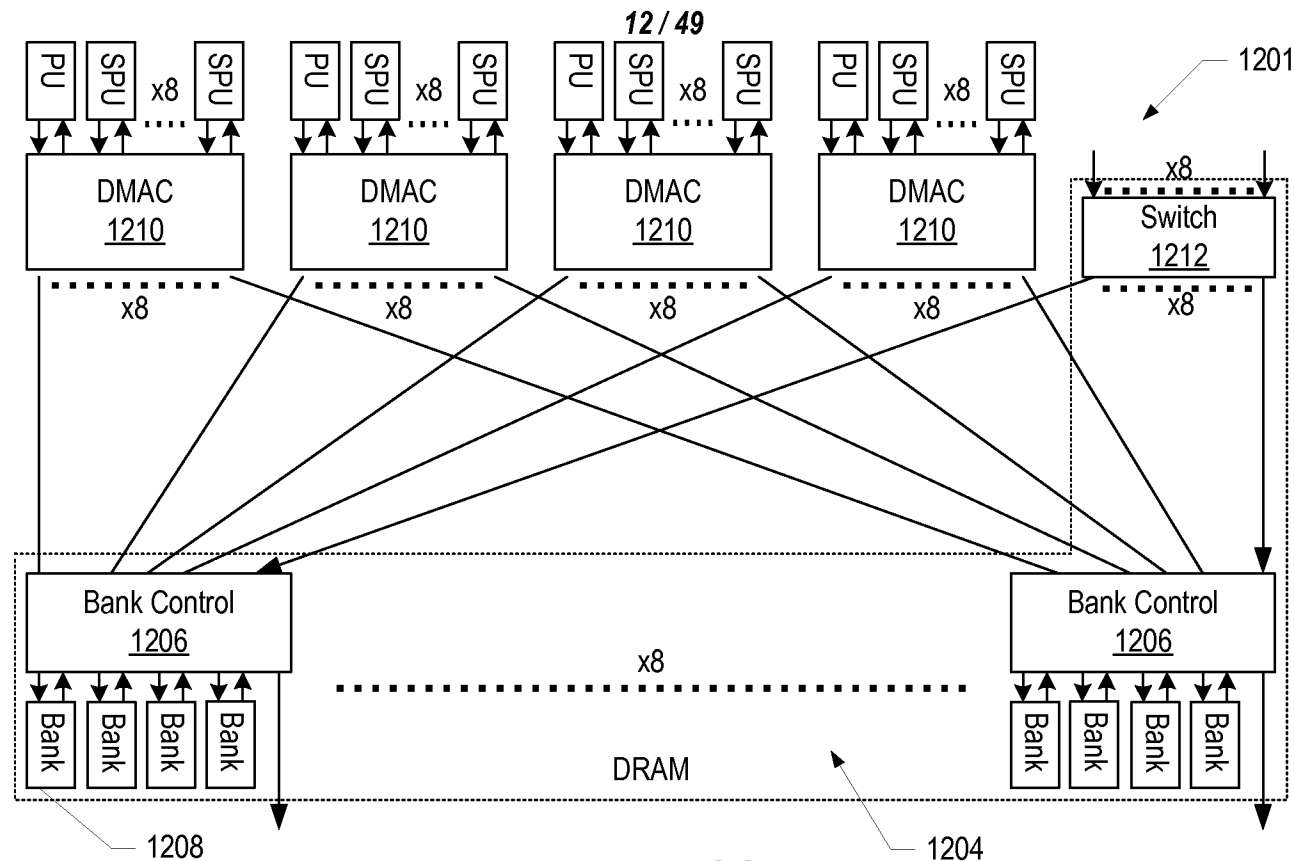


Figure 12A

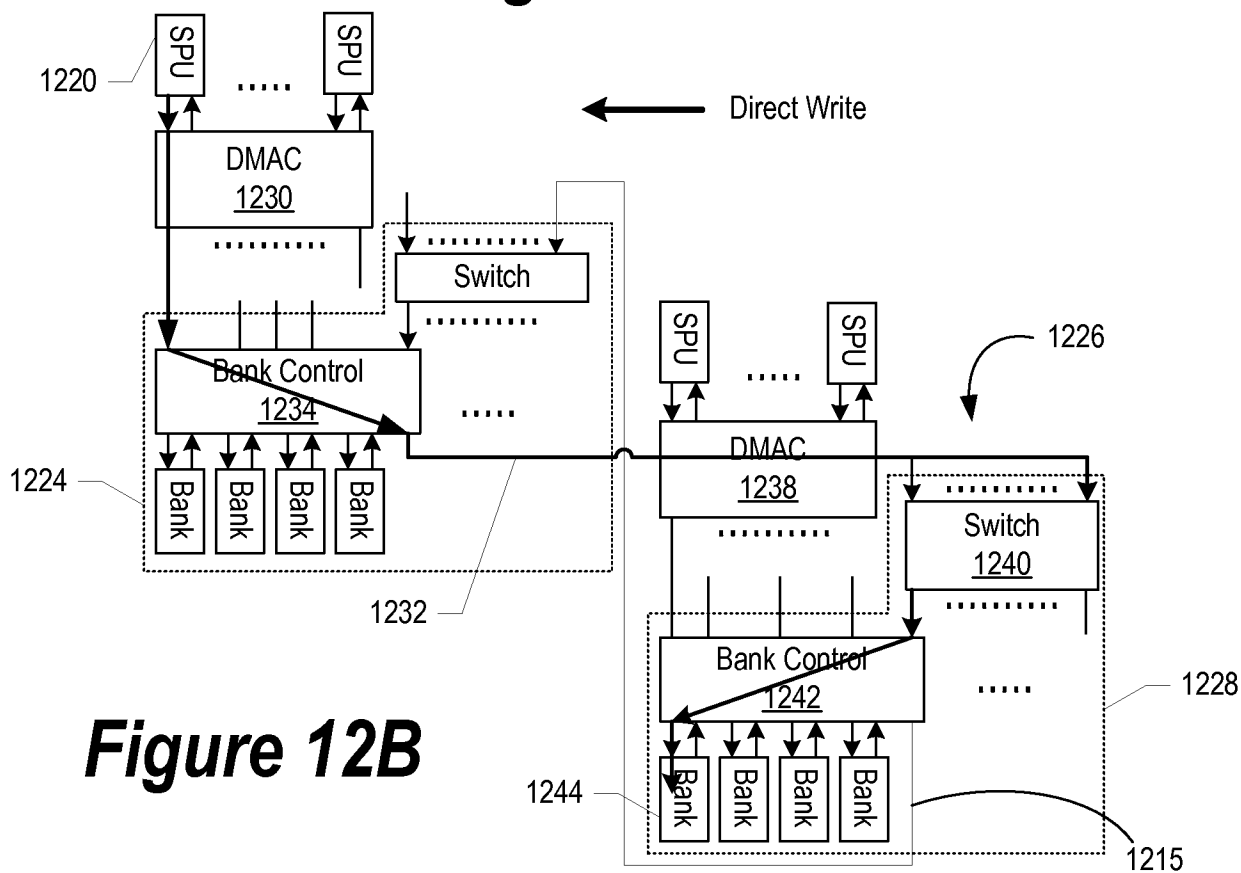


Figure 12B

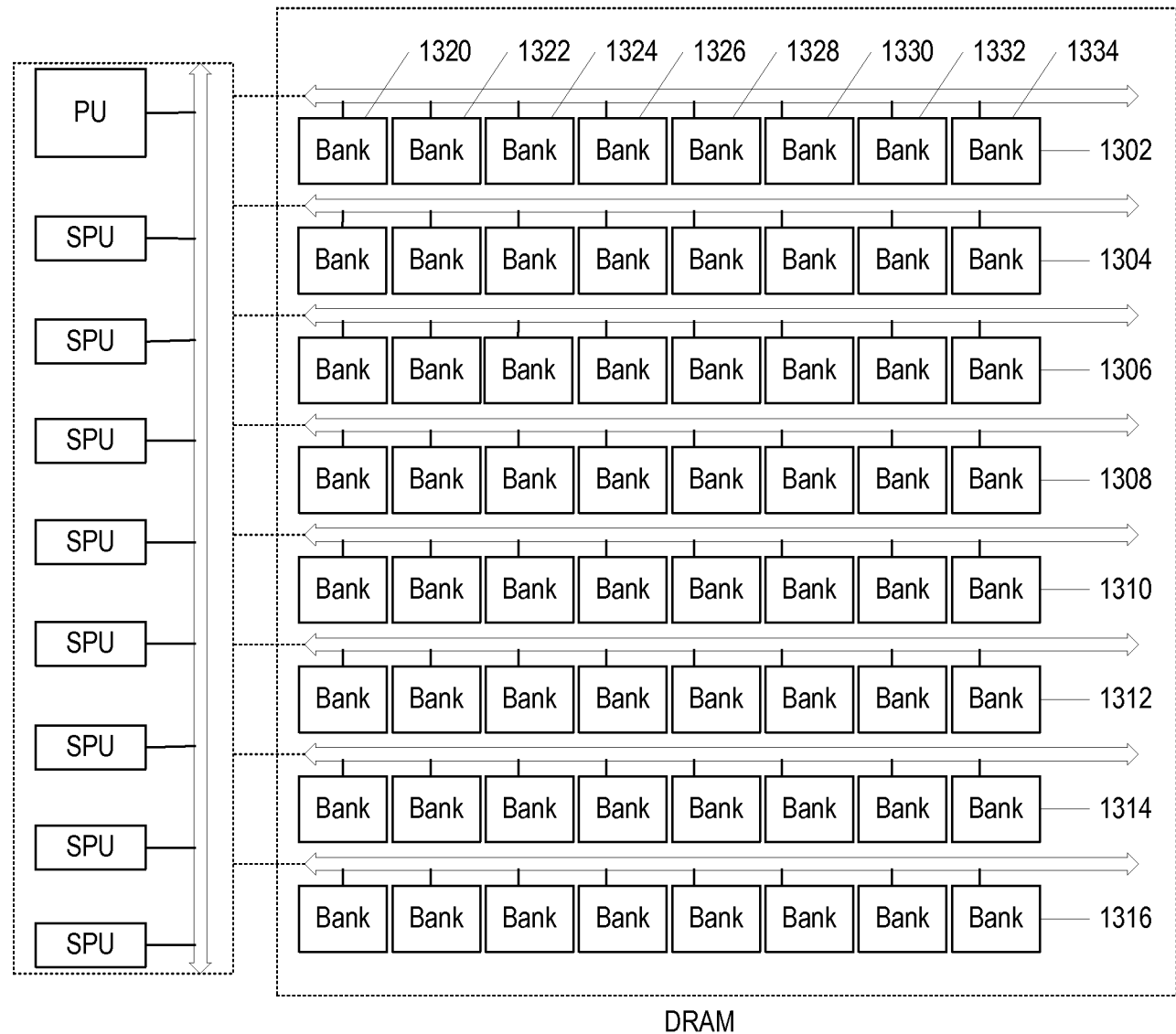


Figure 13

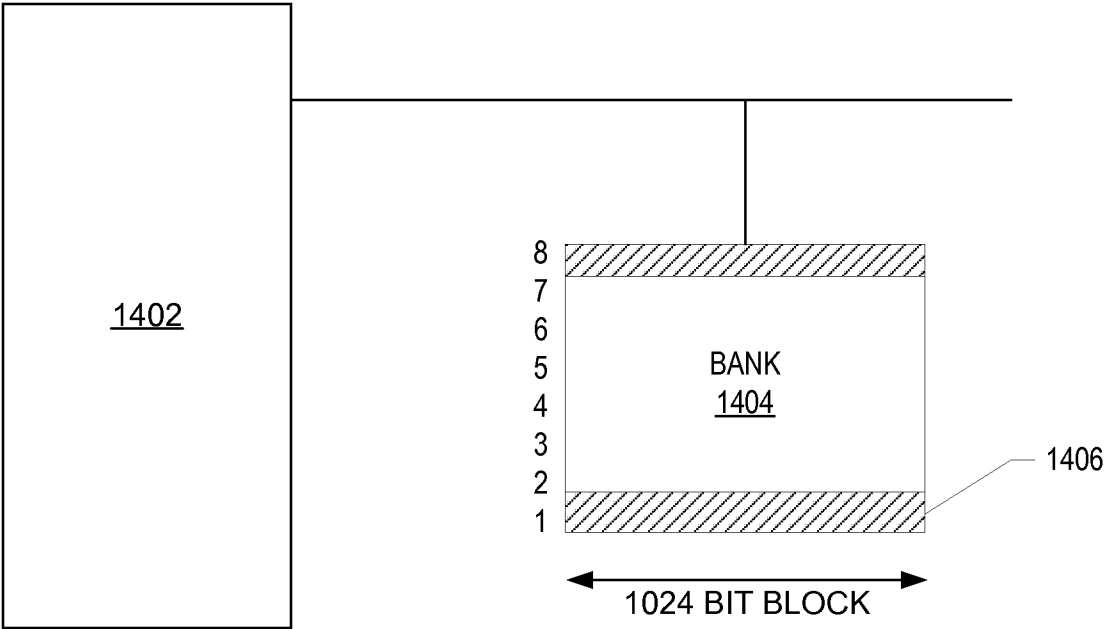


Figure 14A

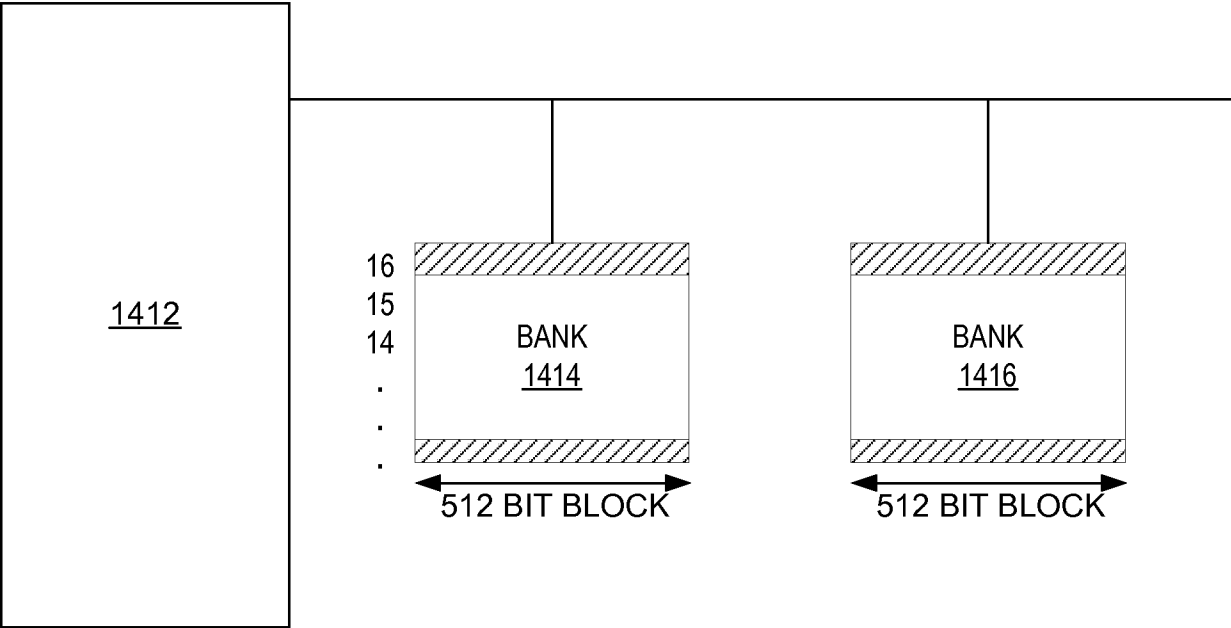


Figure 14B

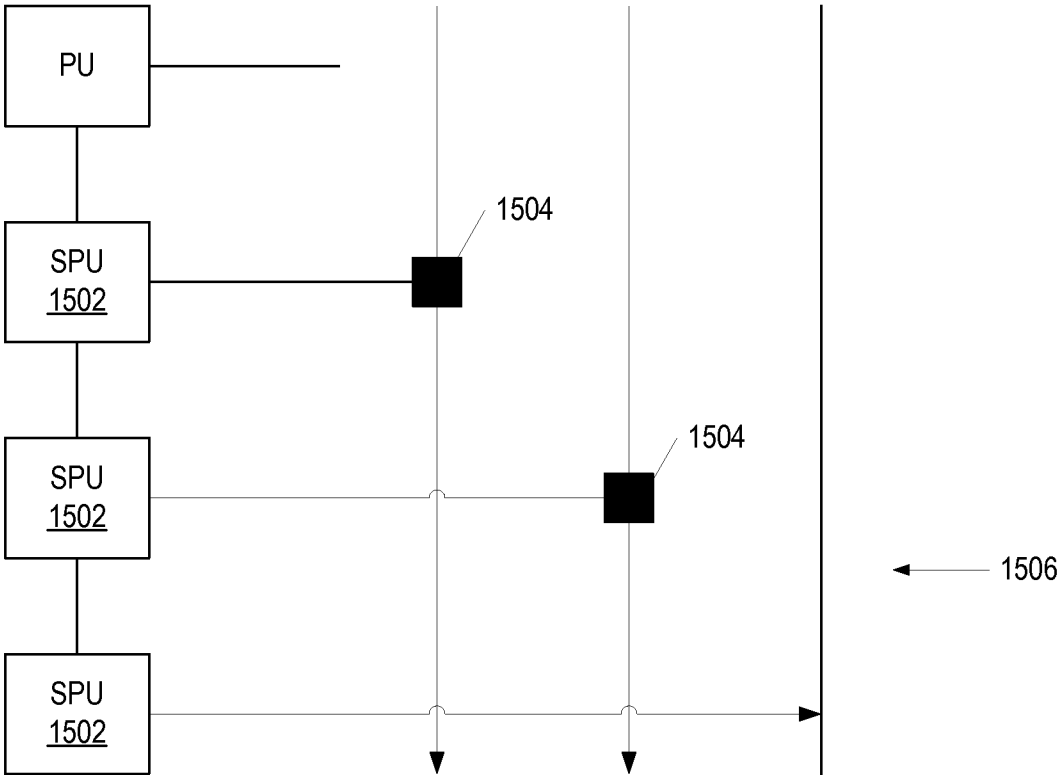


Figure 15

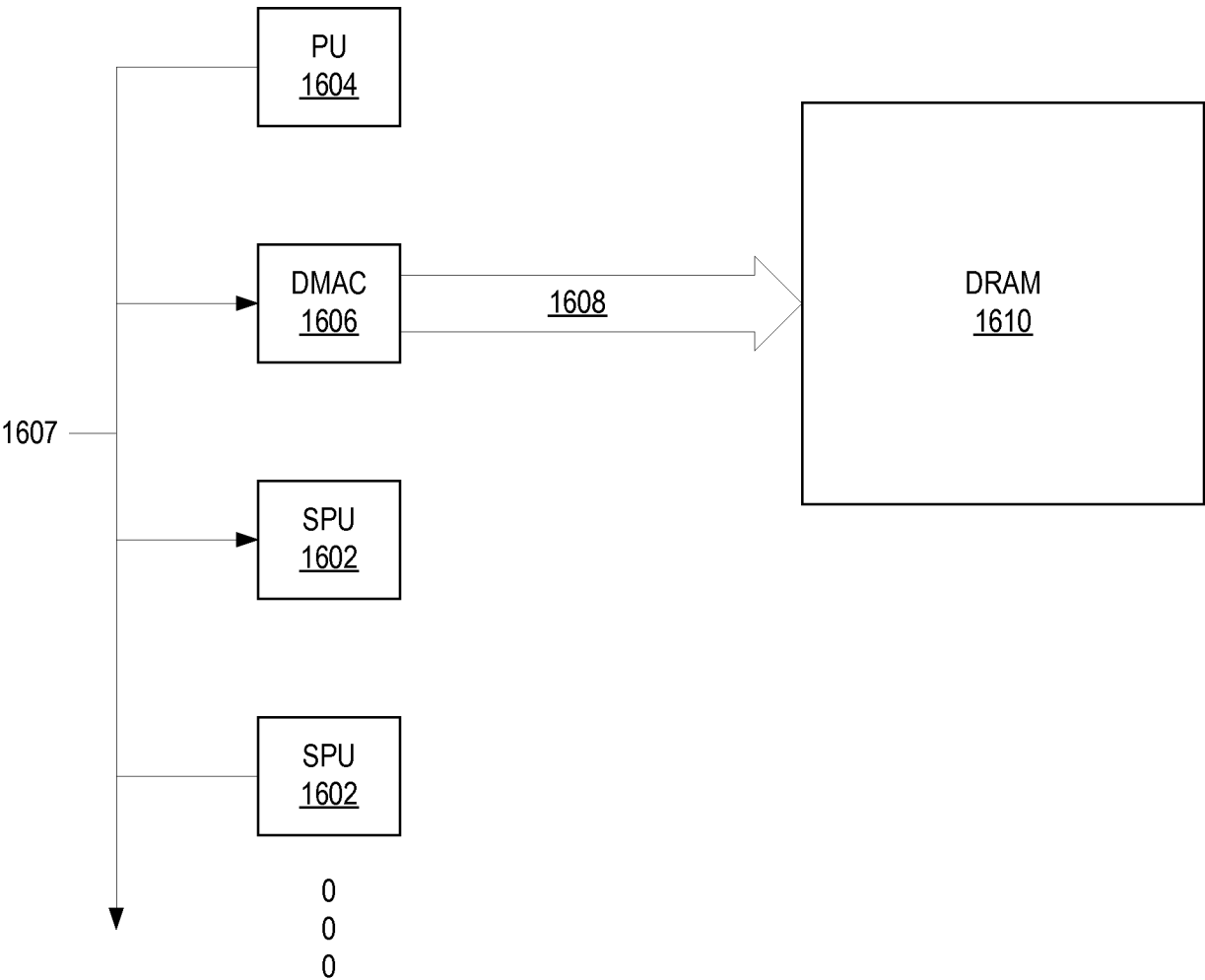


Figure 16

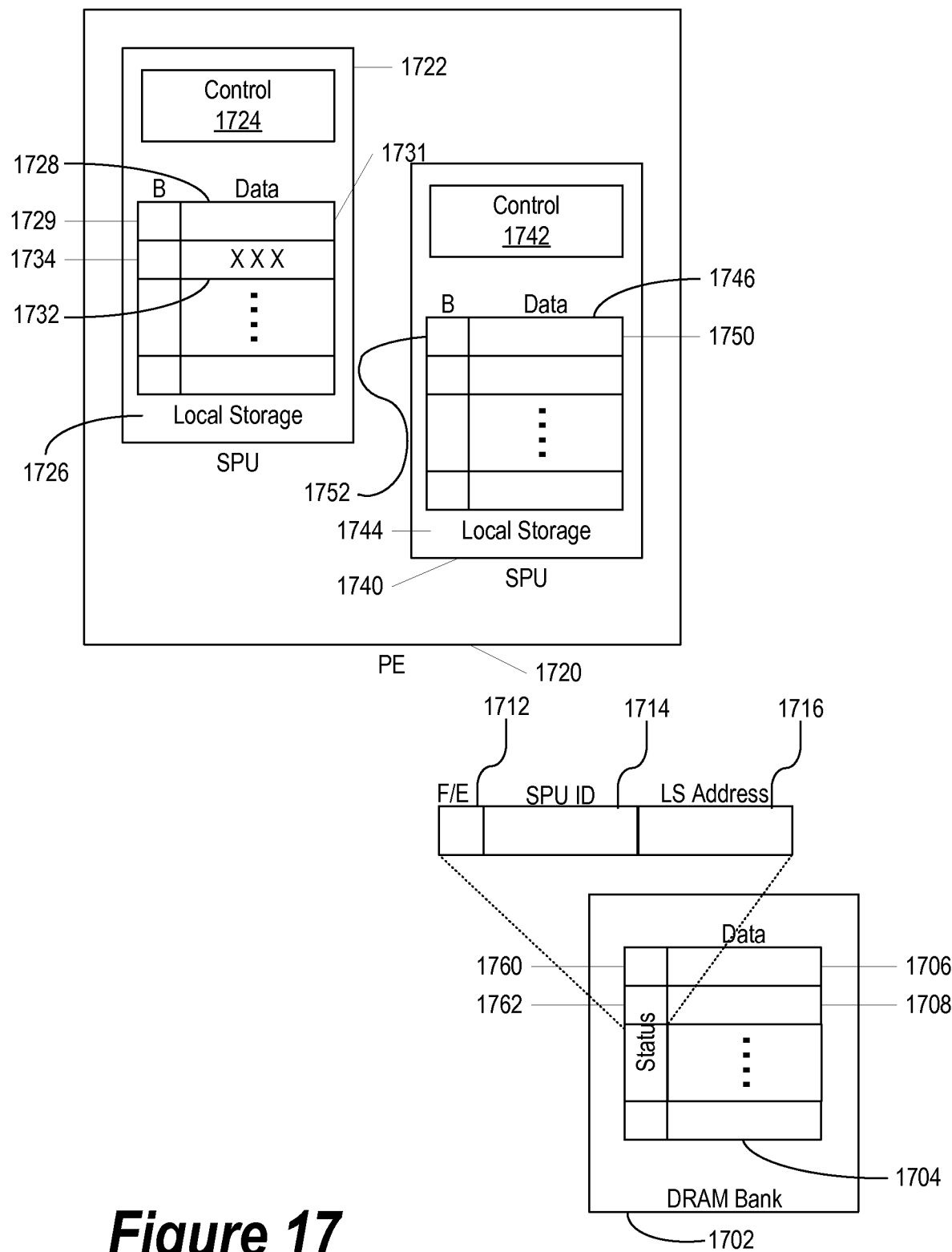


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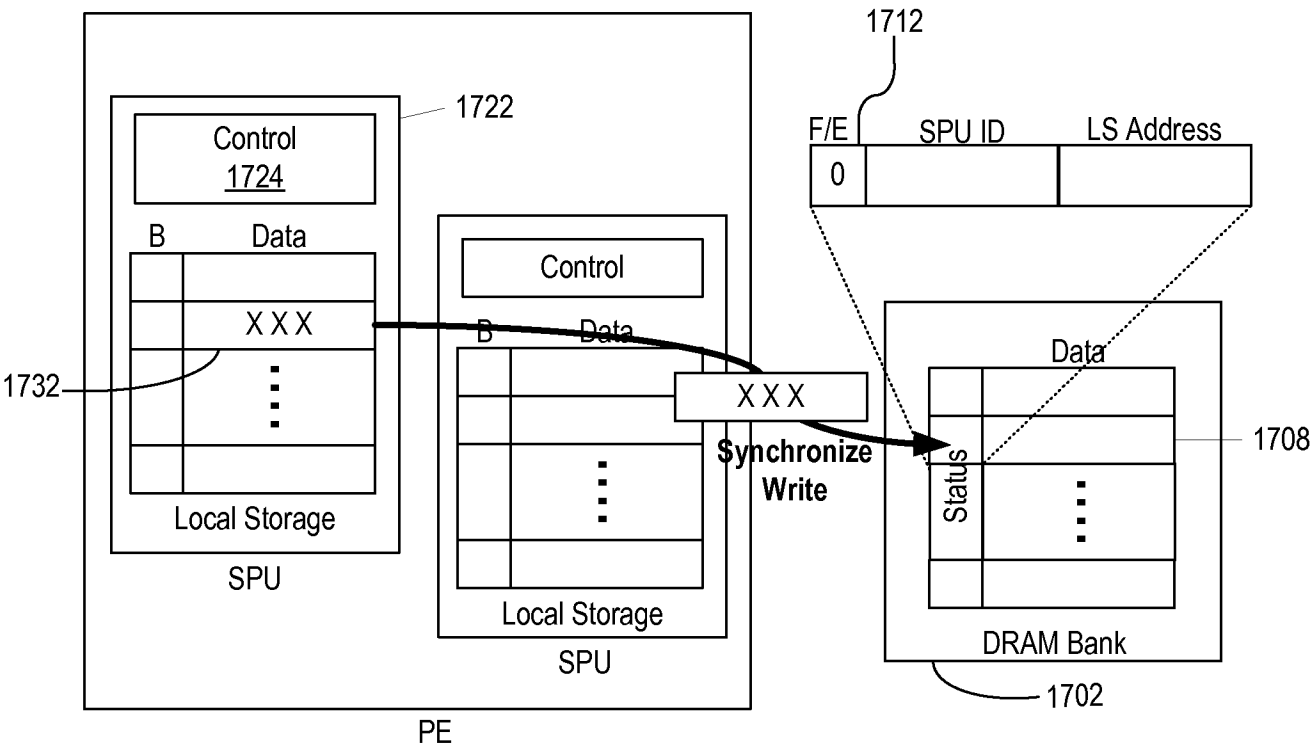


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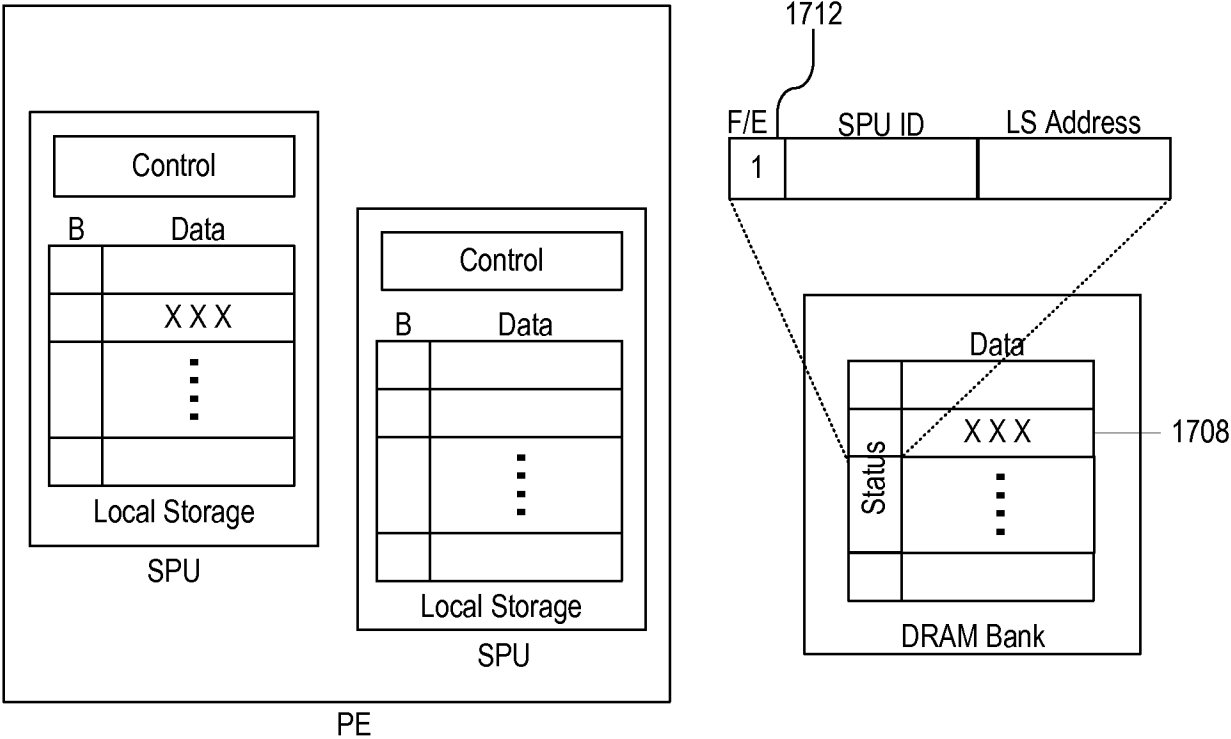


Figure 19

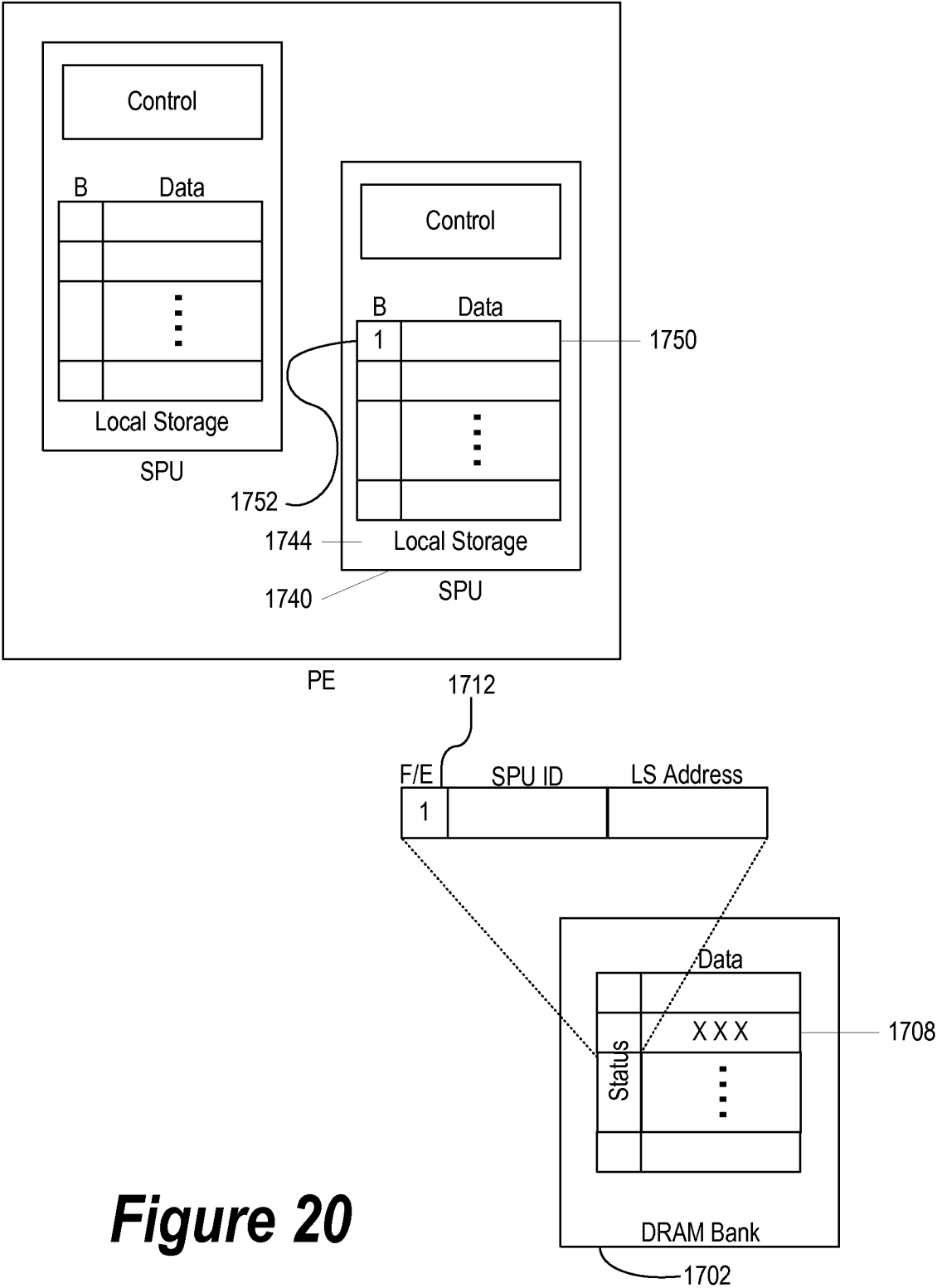


Figure 20

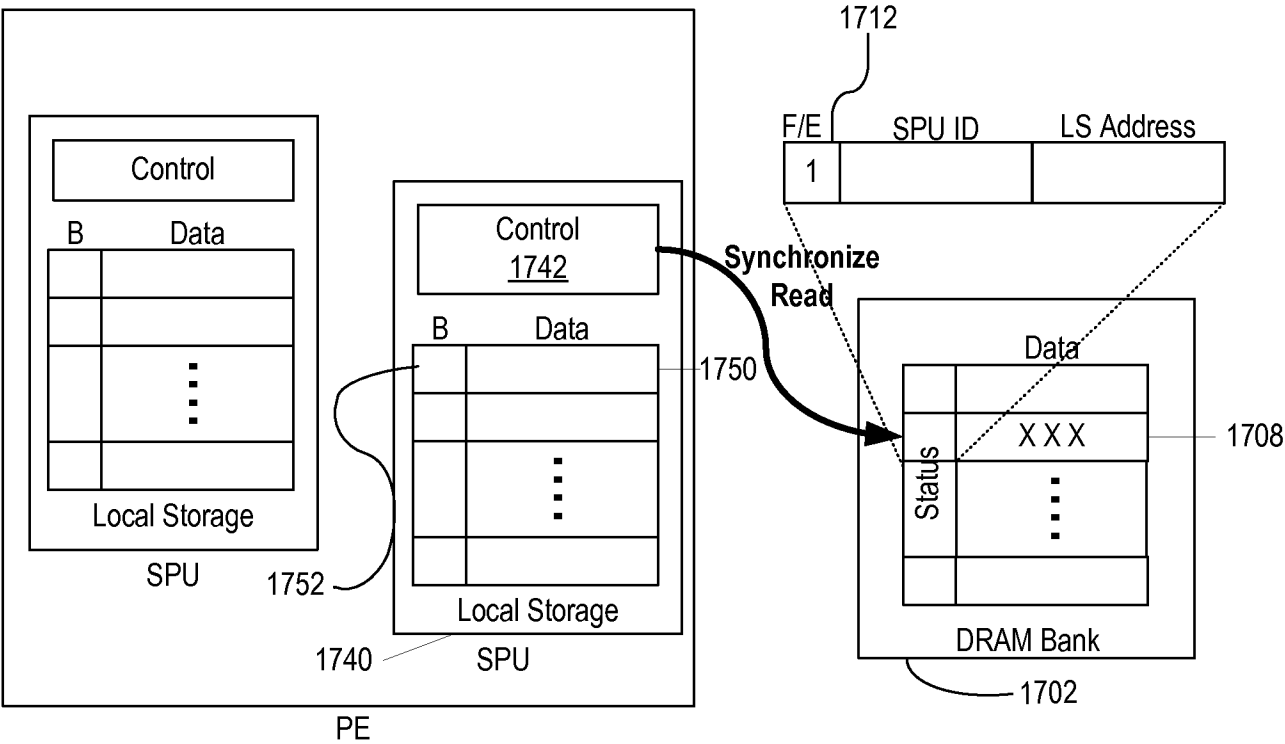
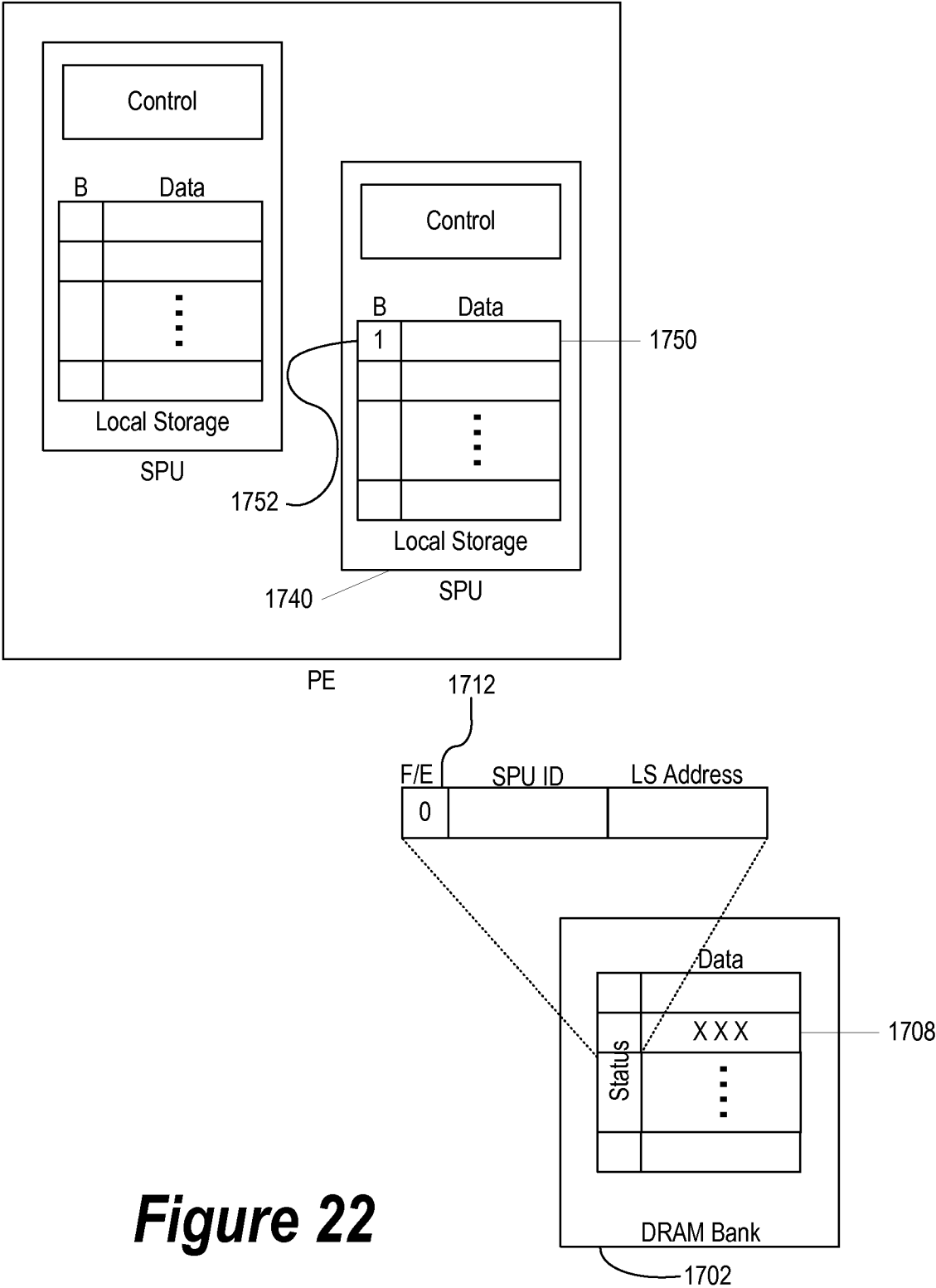


Figure 21



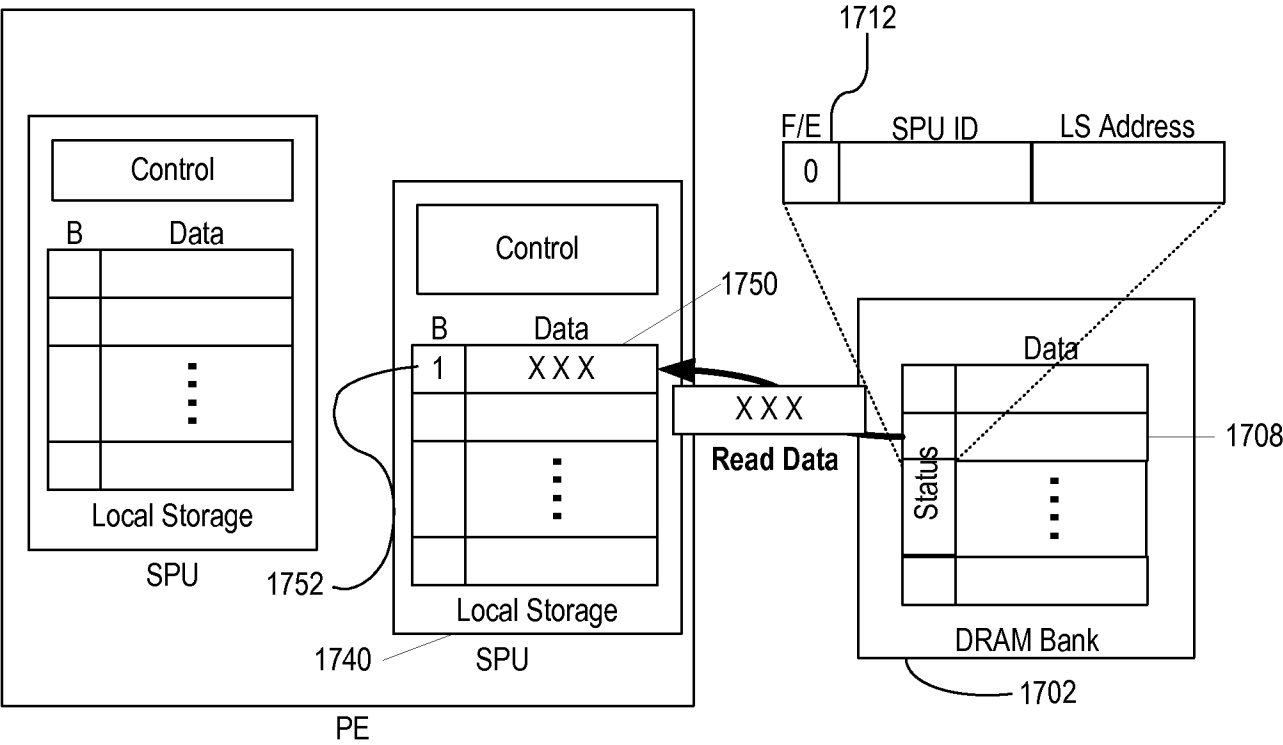
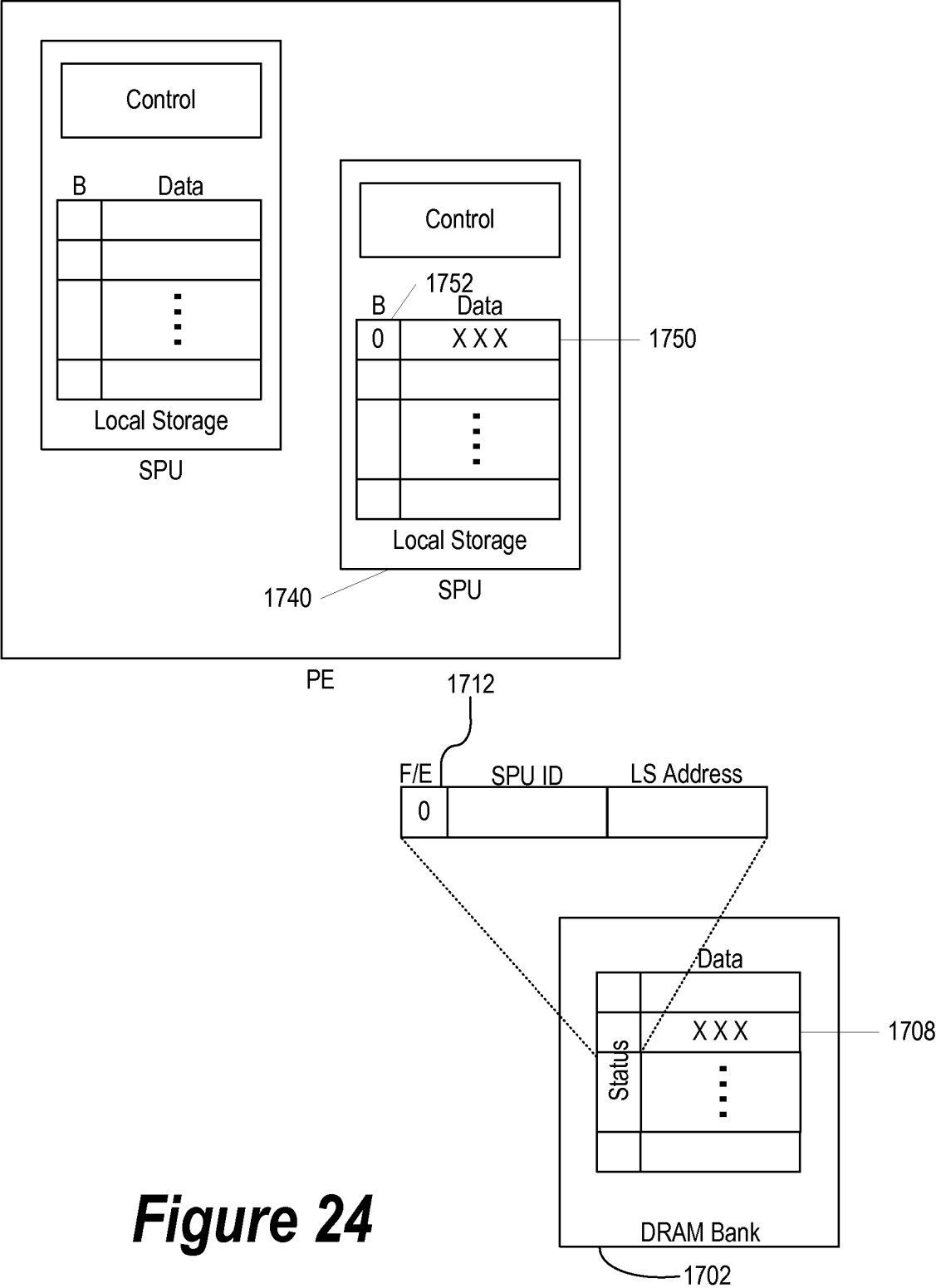
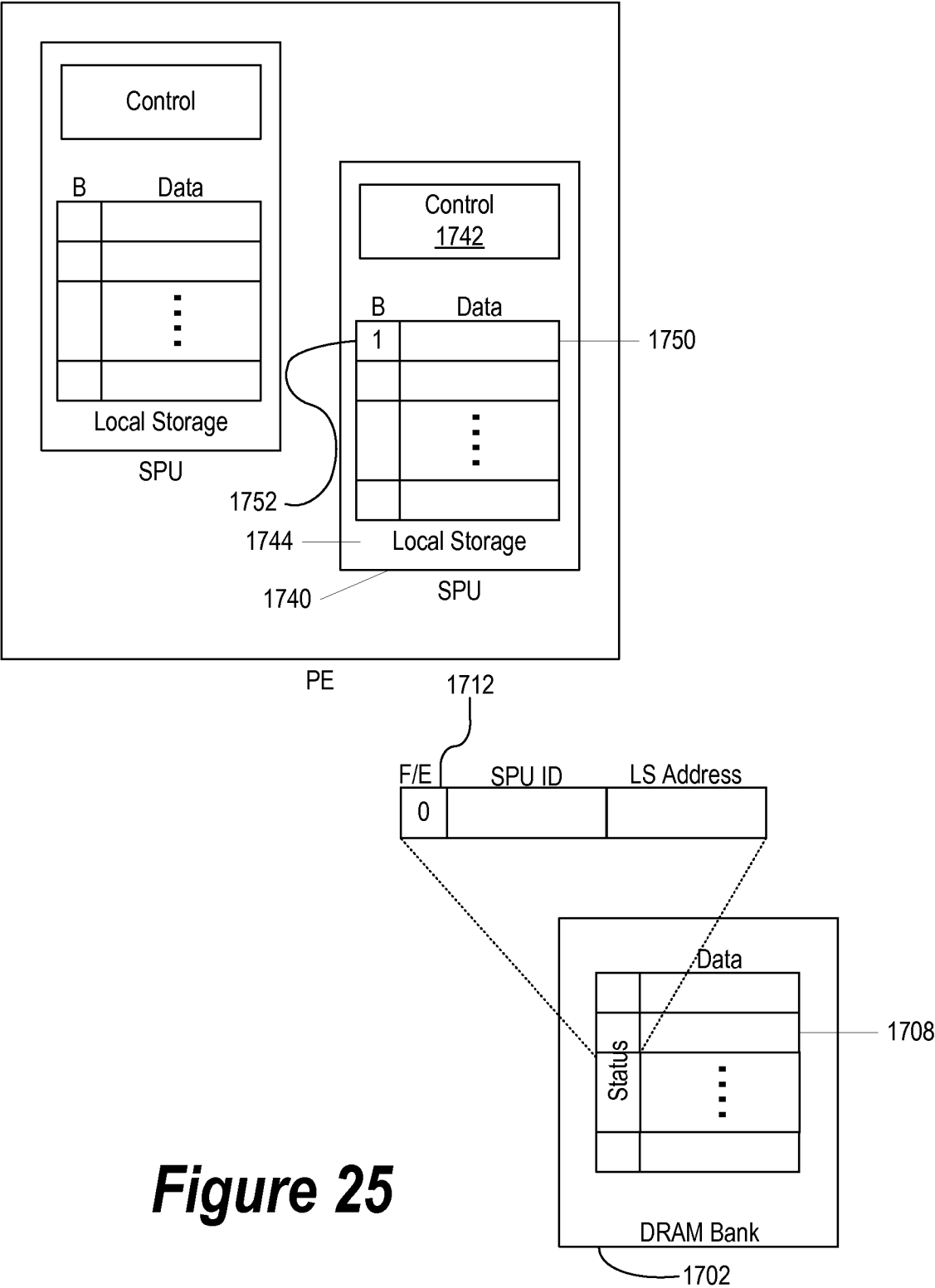


Figure 23





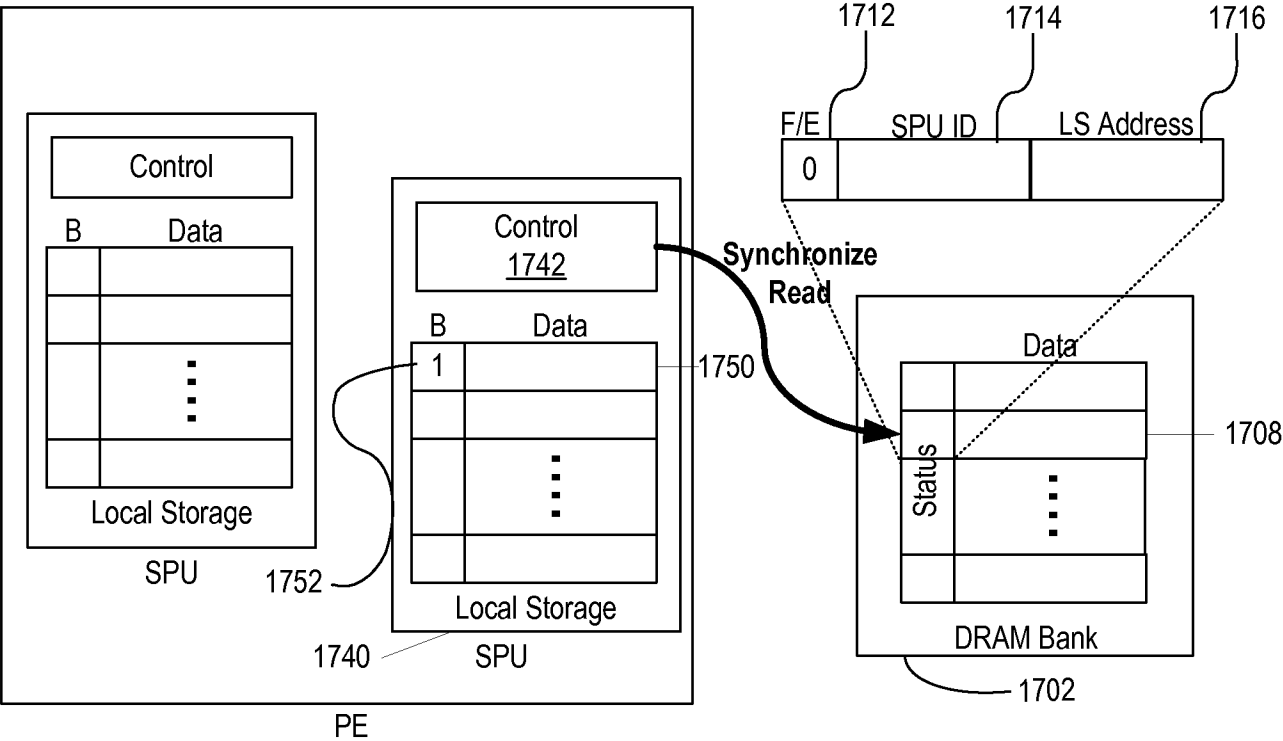


Figure 26

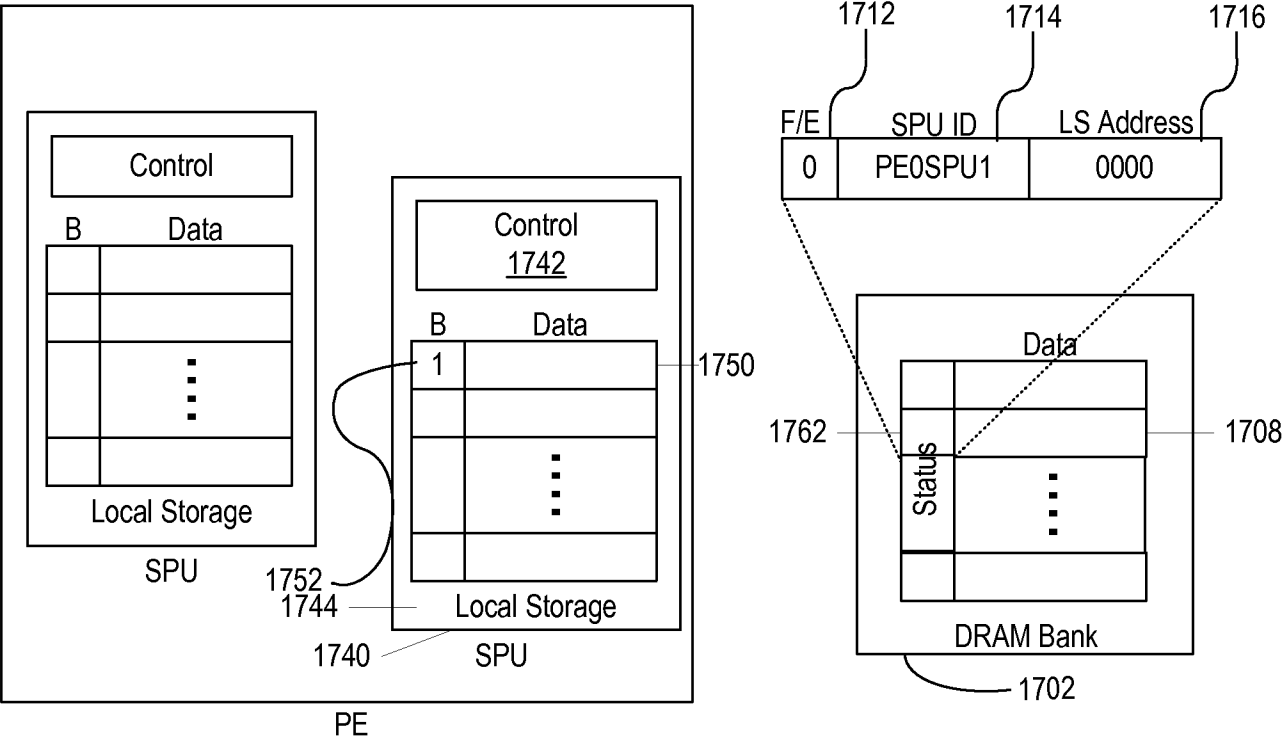


Figure 27

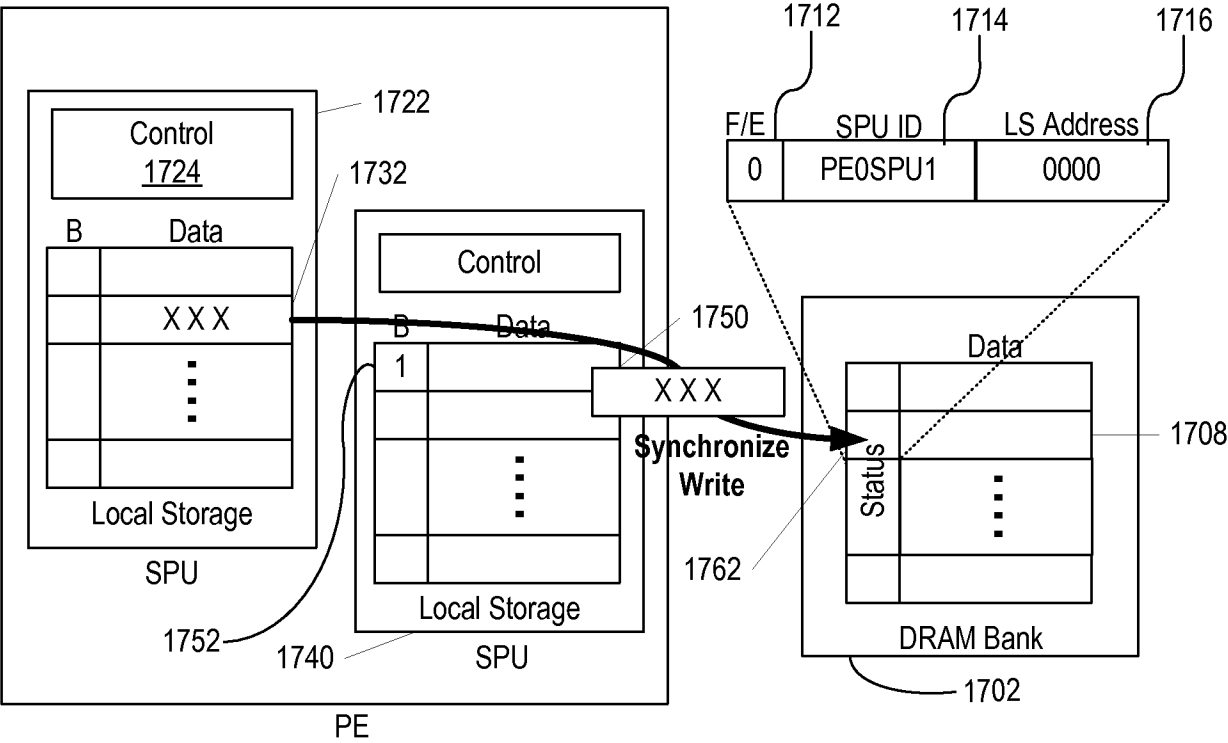


Figure 28

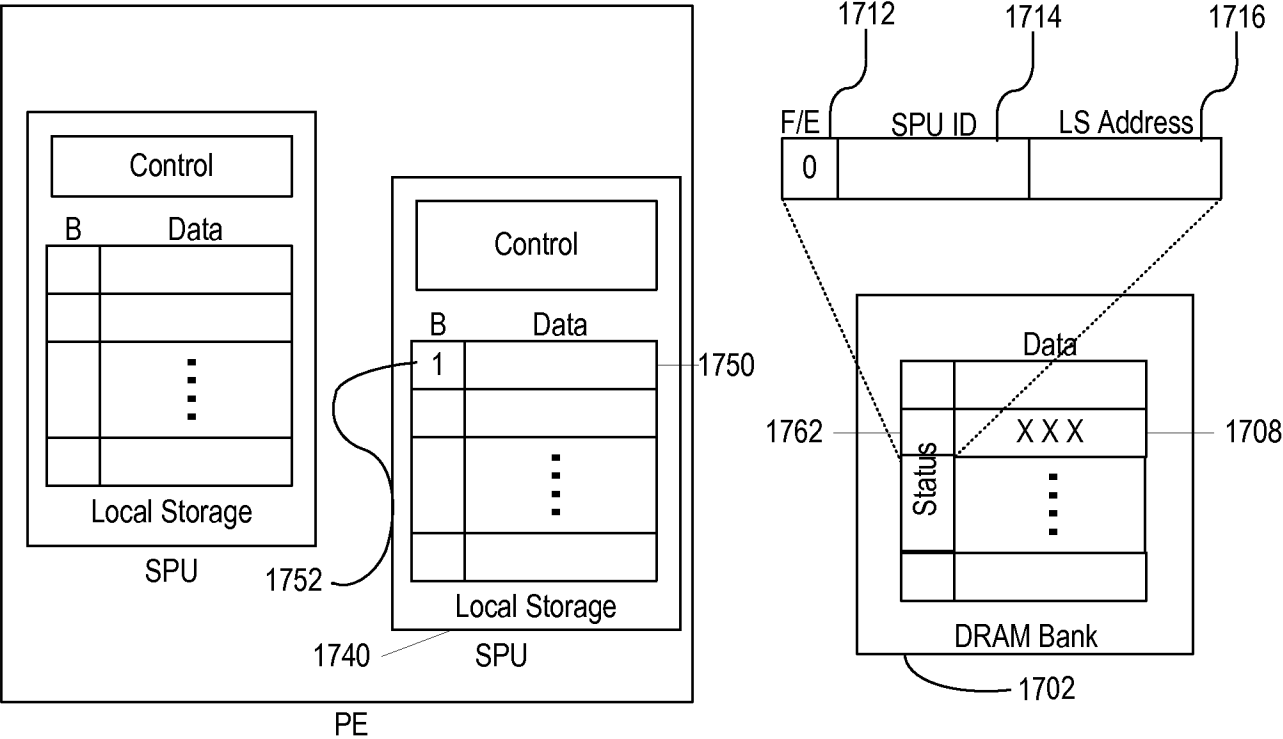


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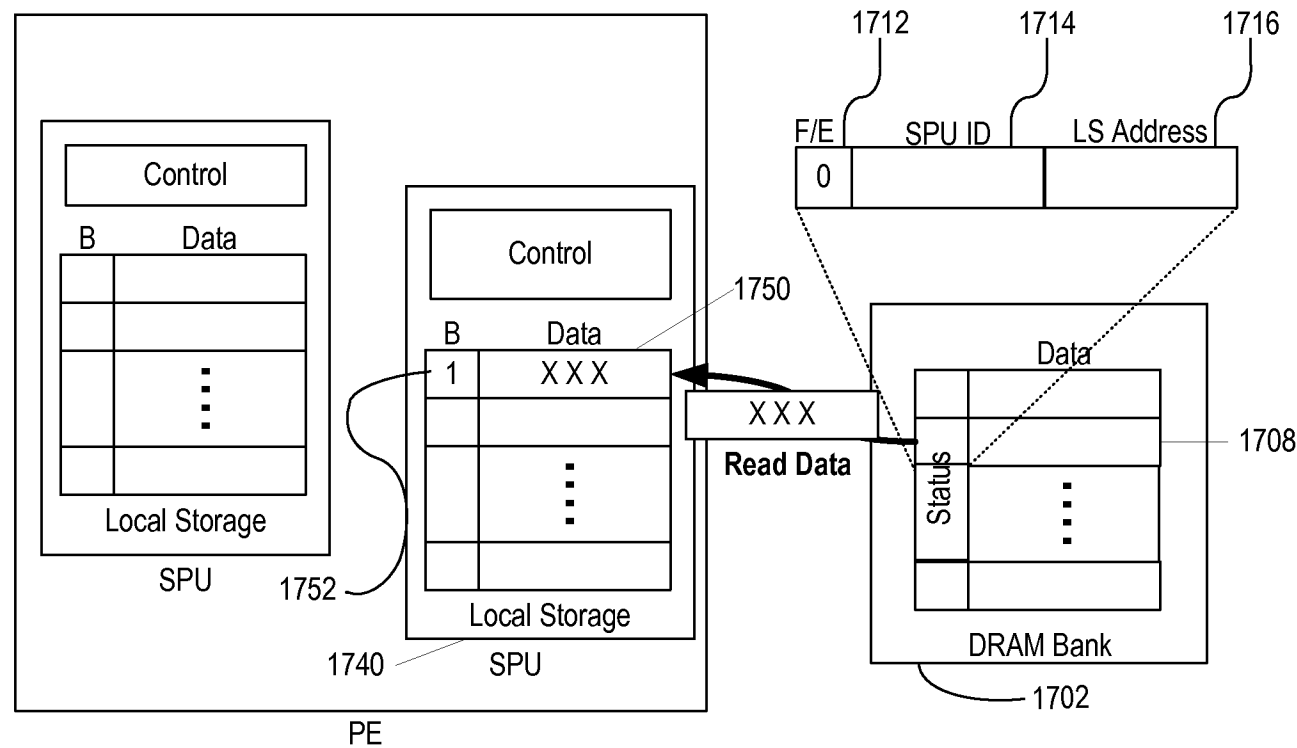


Figure 30

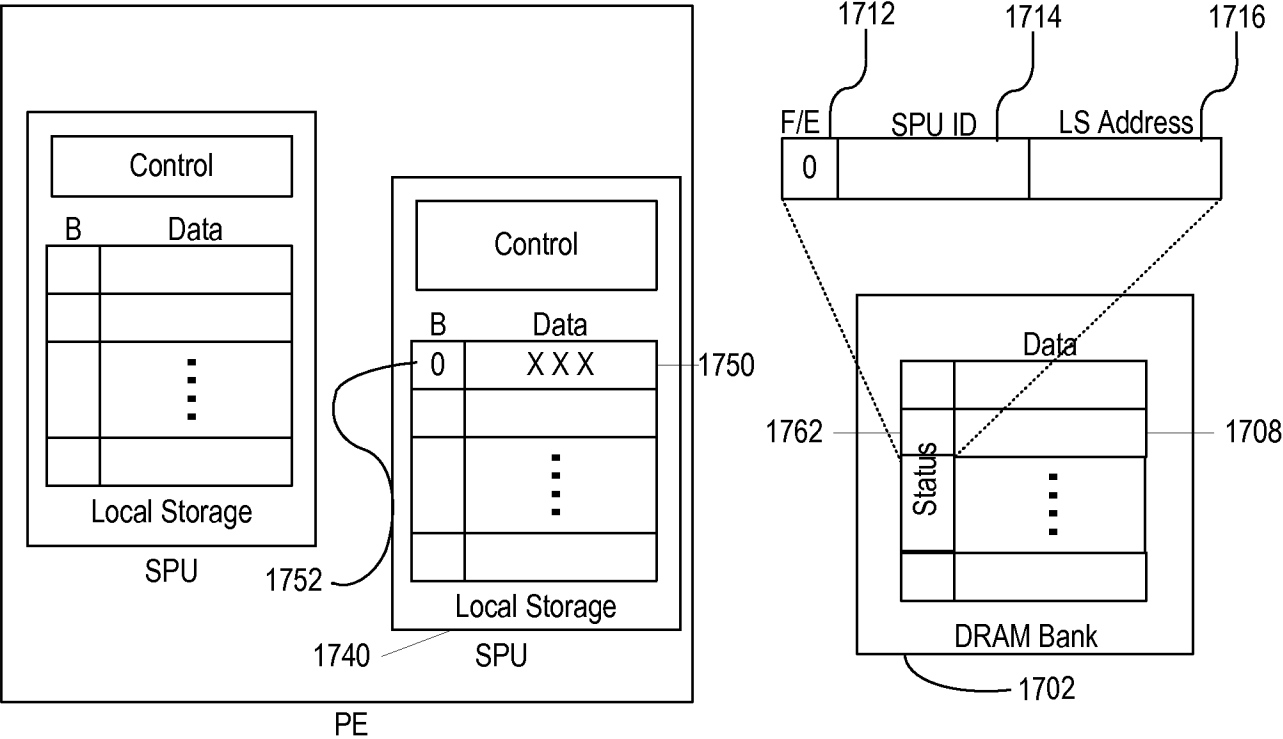


Figure 31

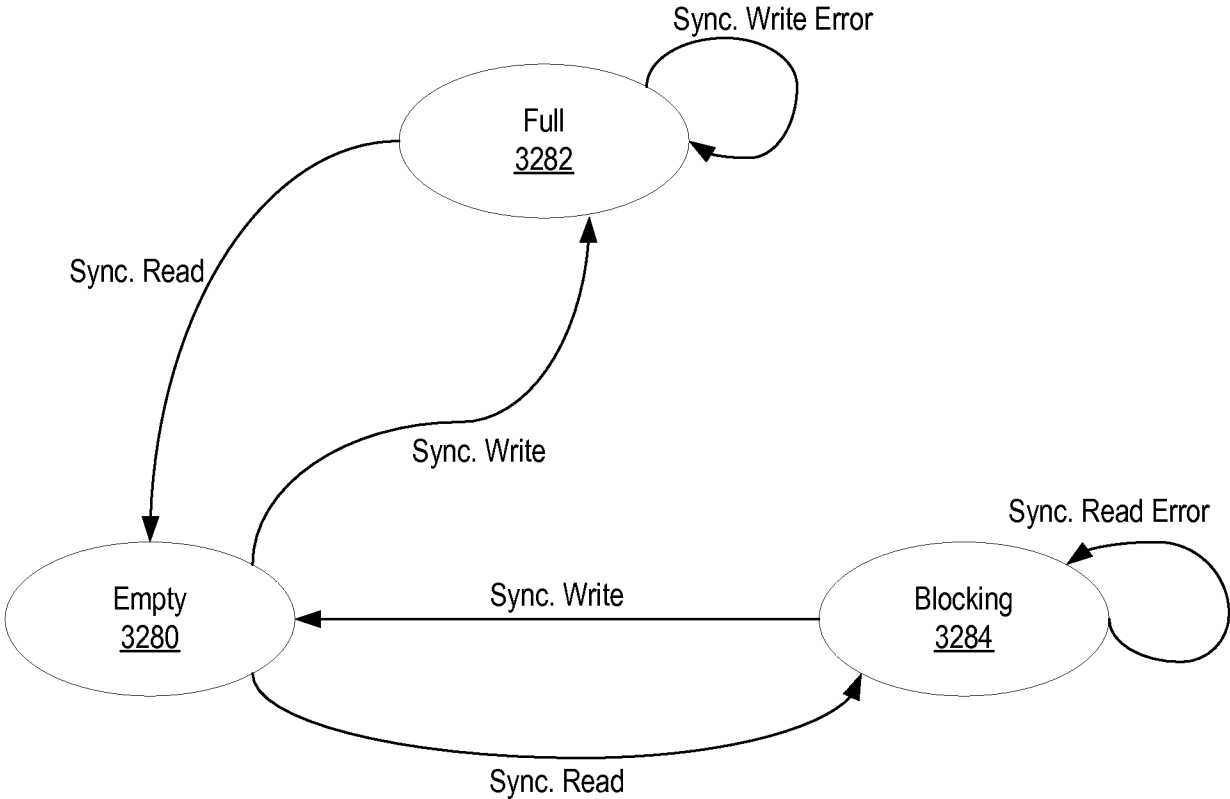


Figure 32

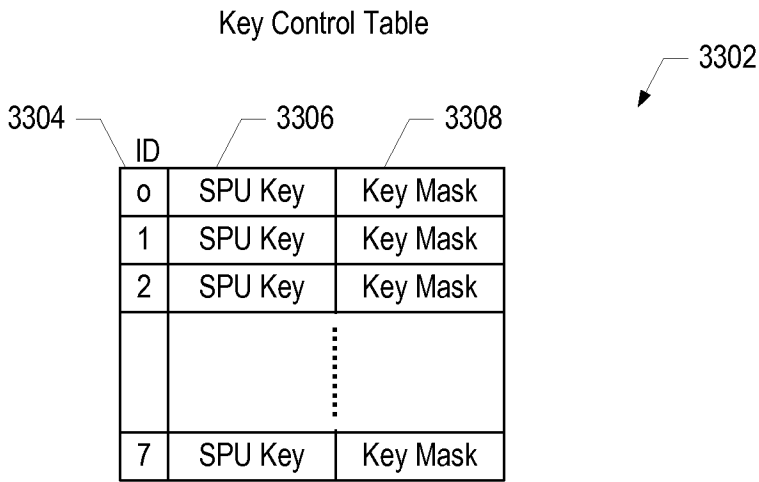


Figure 33

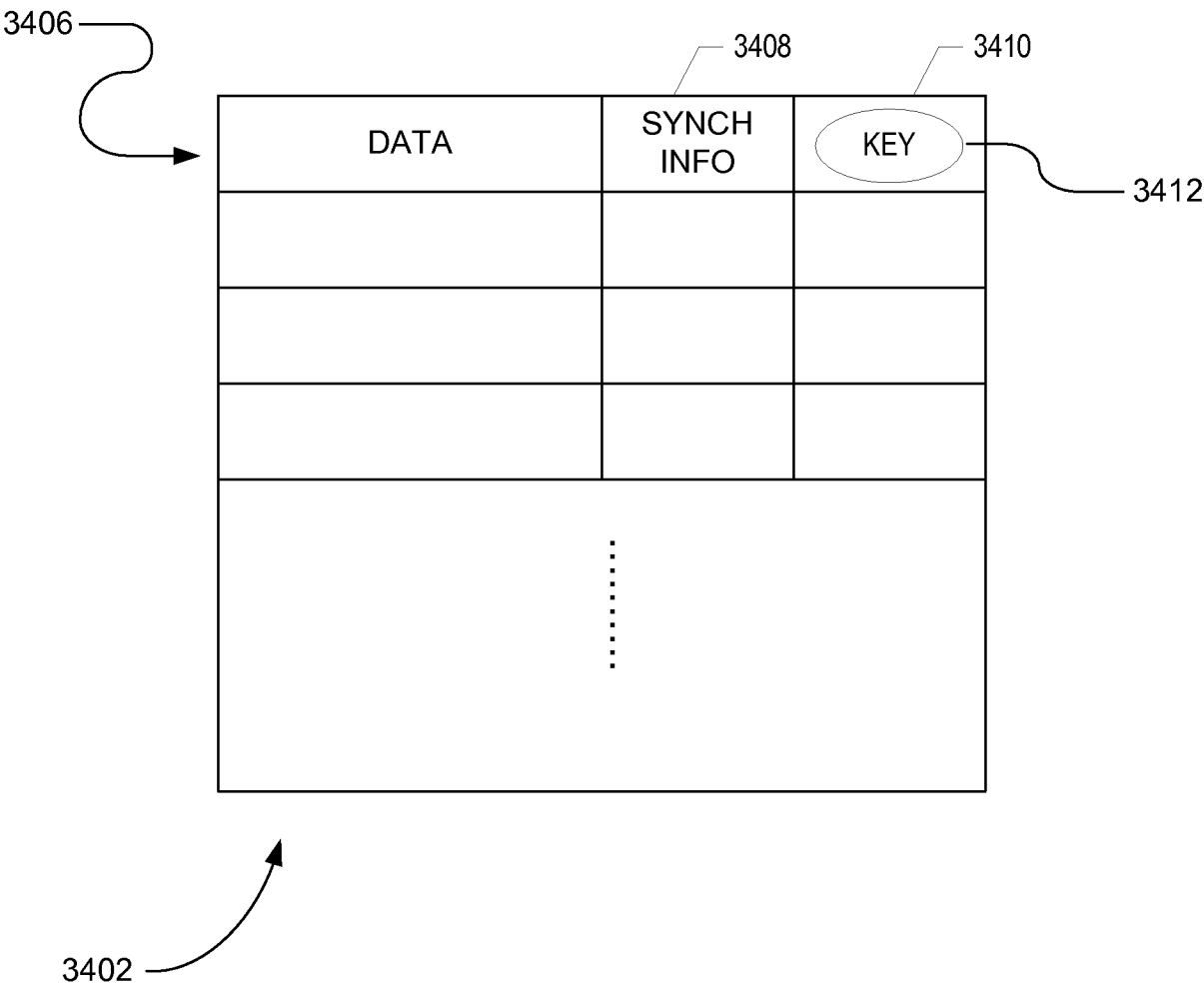


Figure 34

Memory Access Control Table

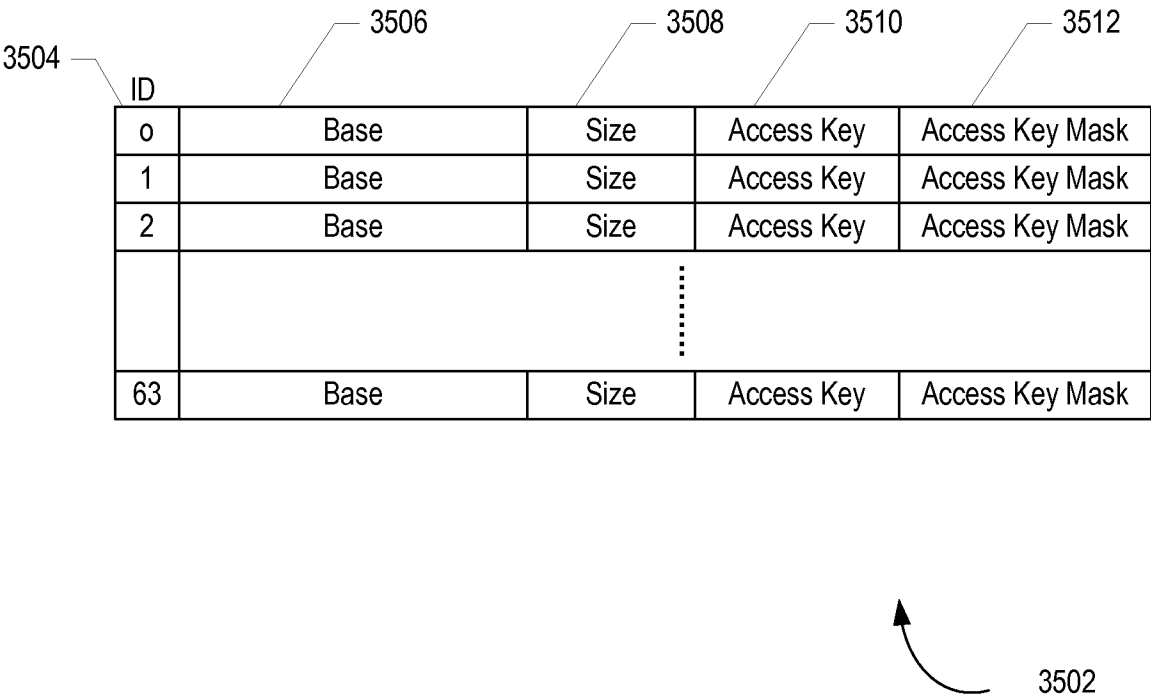
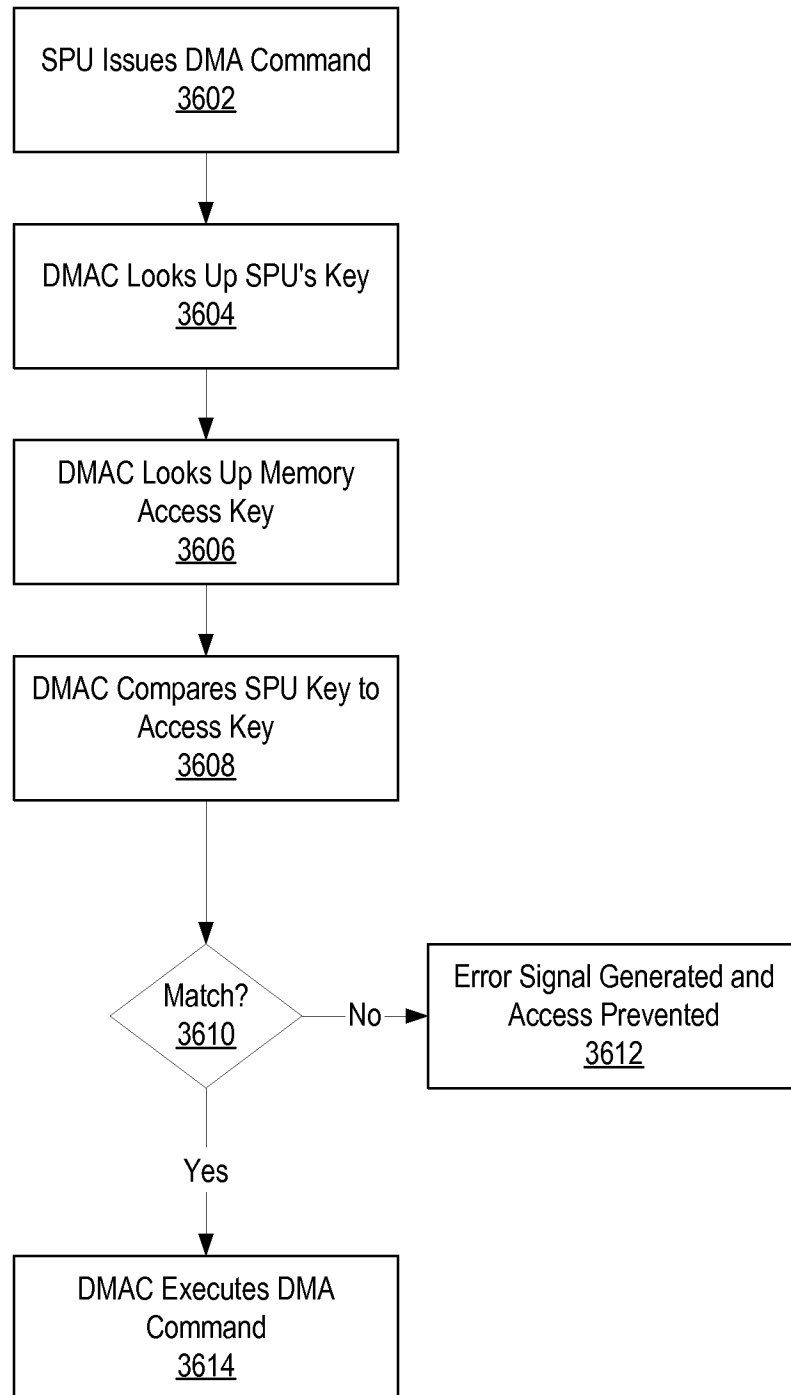


Figure 35

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**Figure 36**

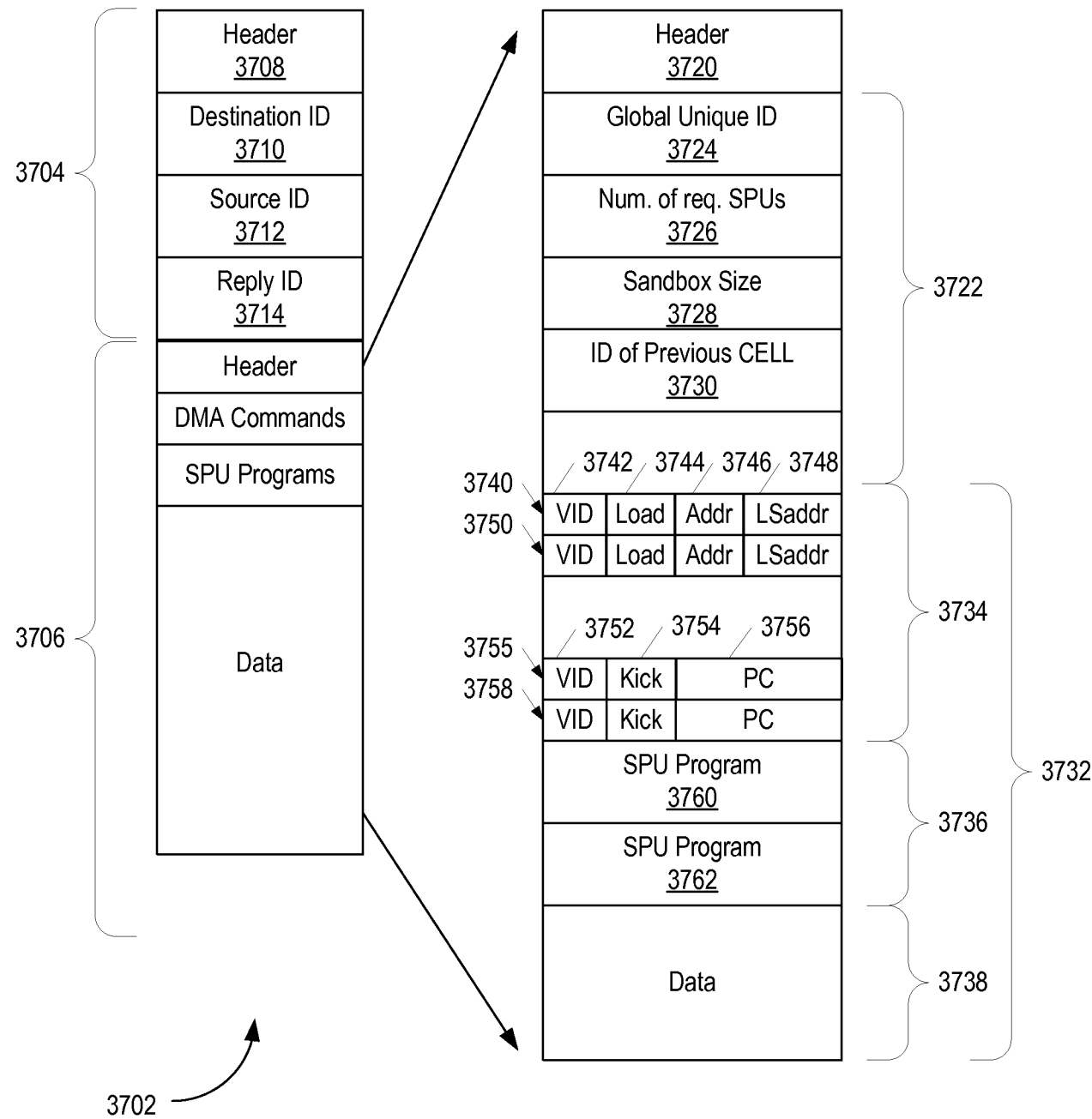
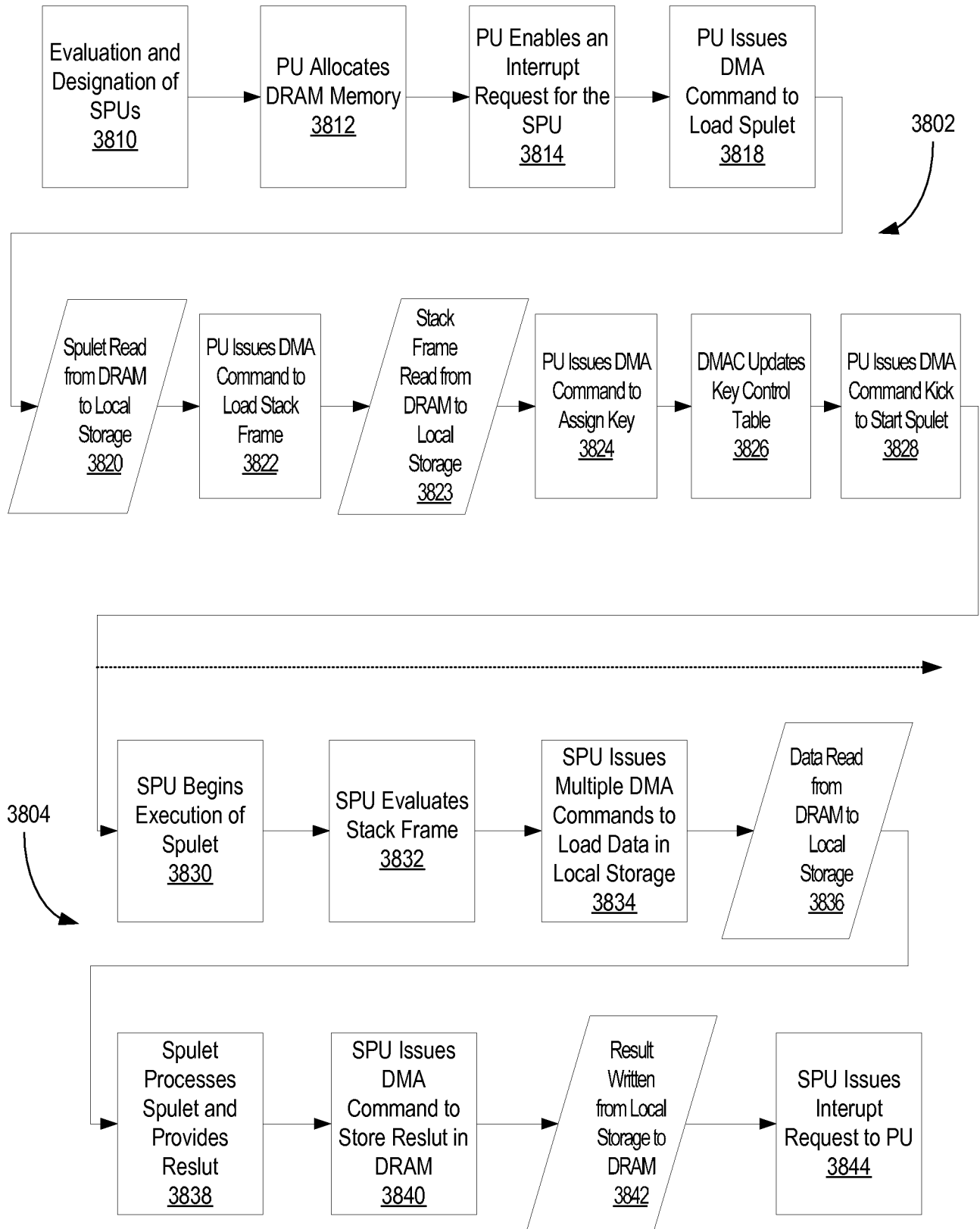


Figure 37

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**Figure 38**

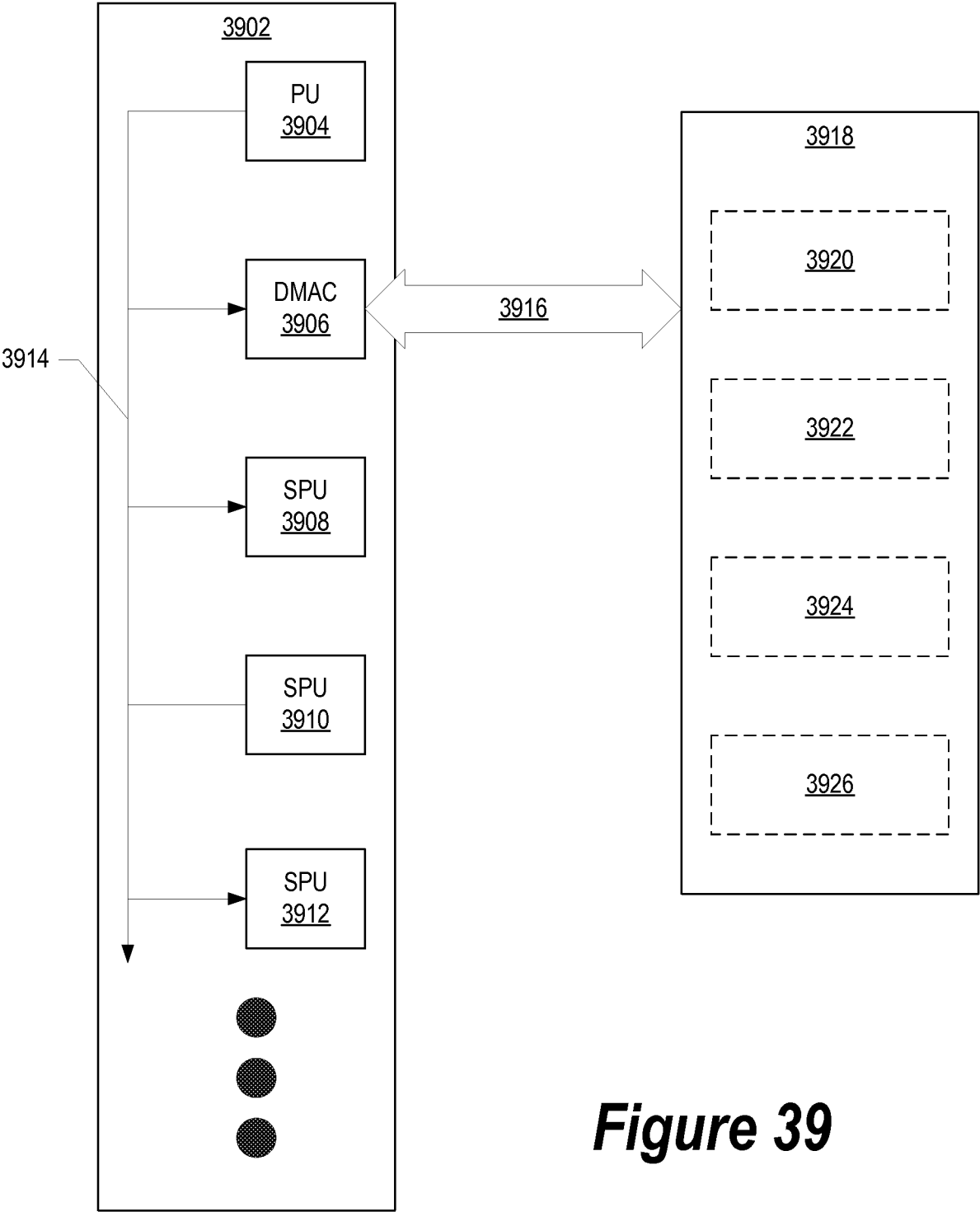
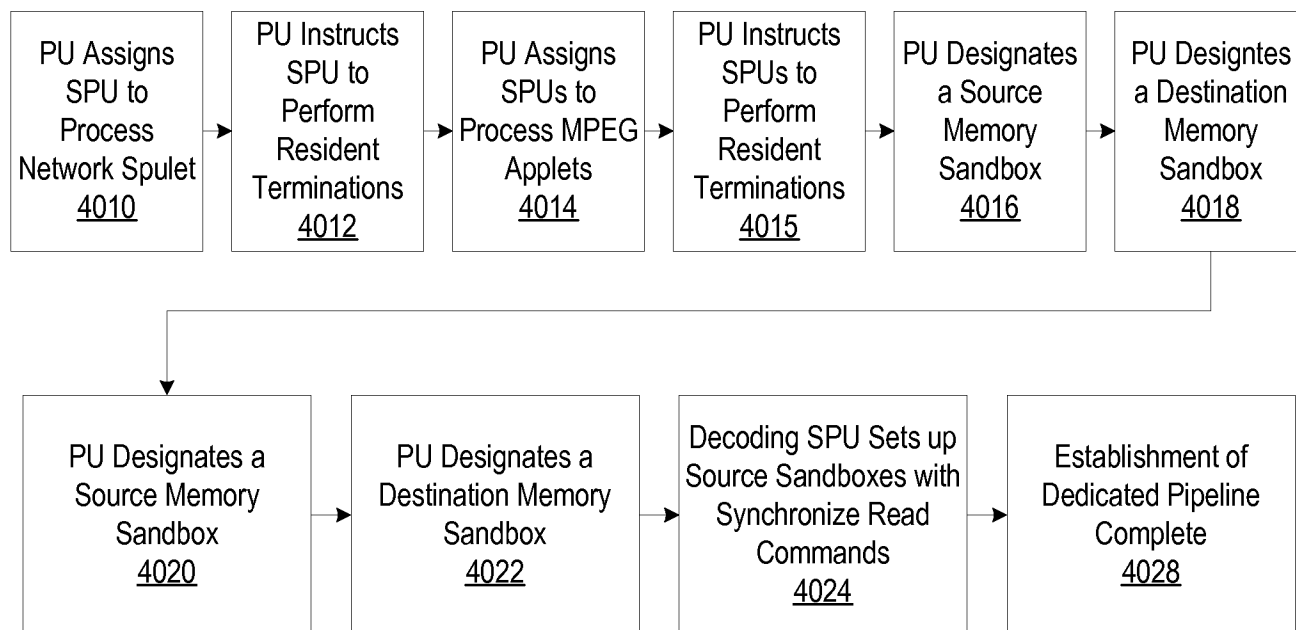
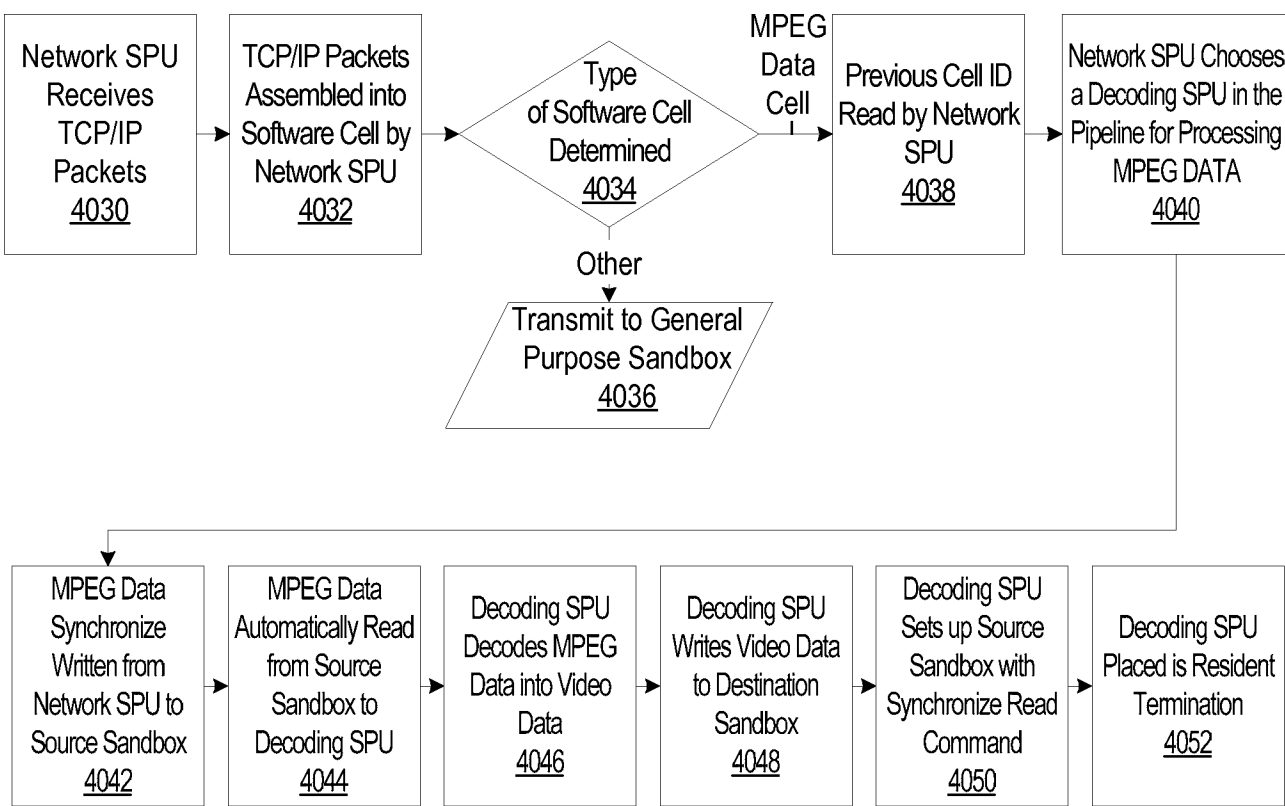


Figure 39

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**Figure 40A****Figure 40B**

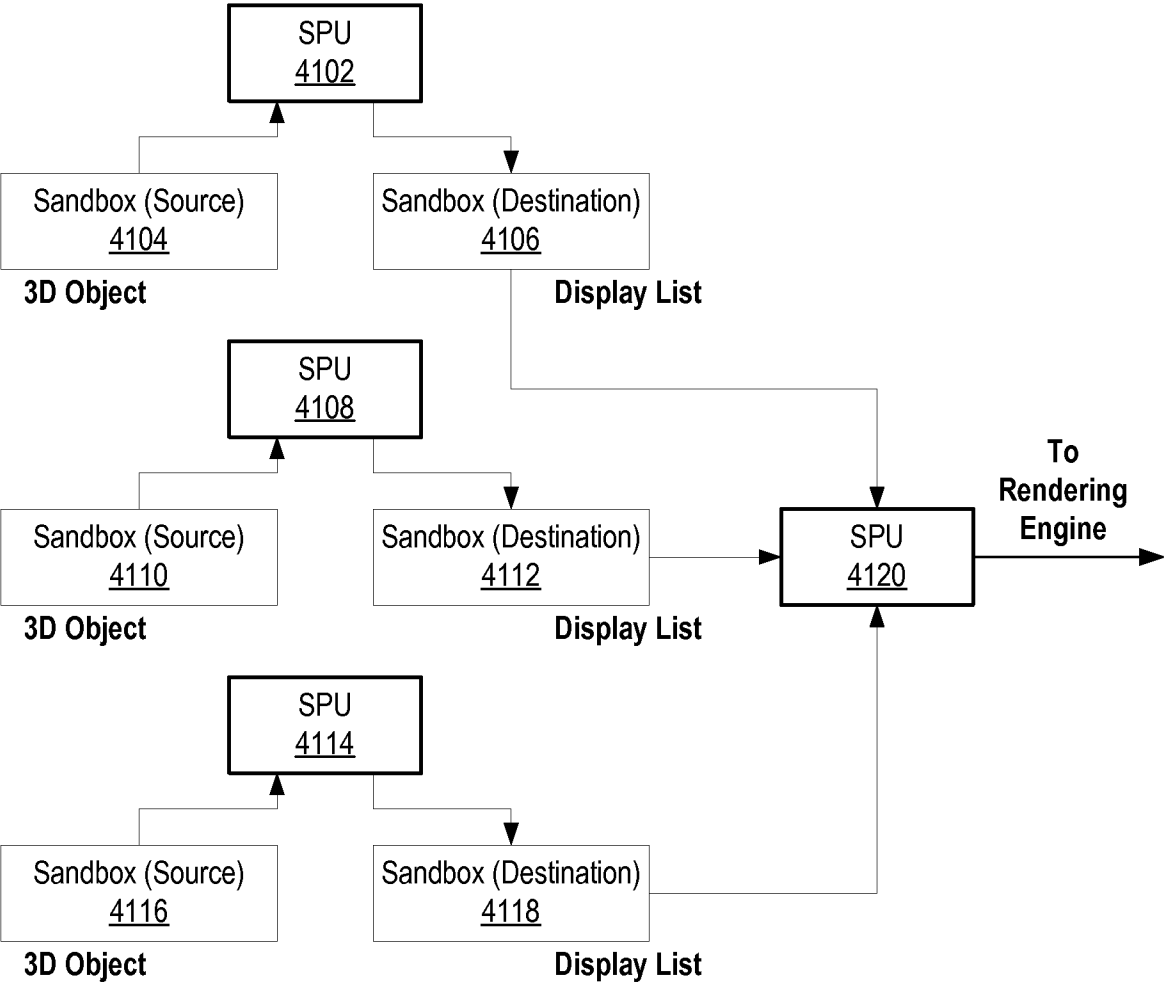


Figure 41

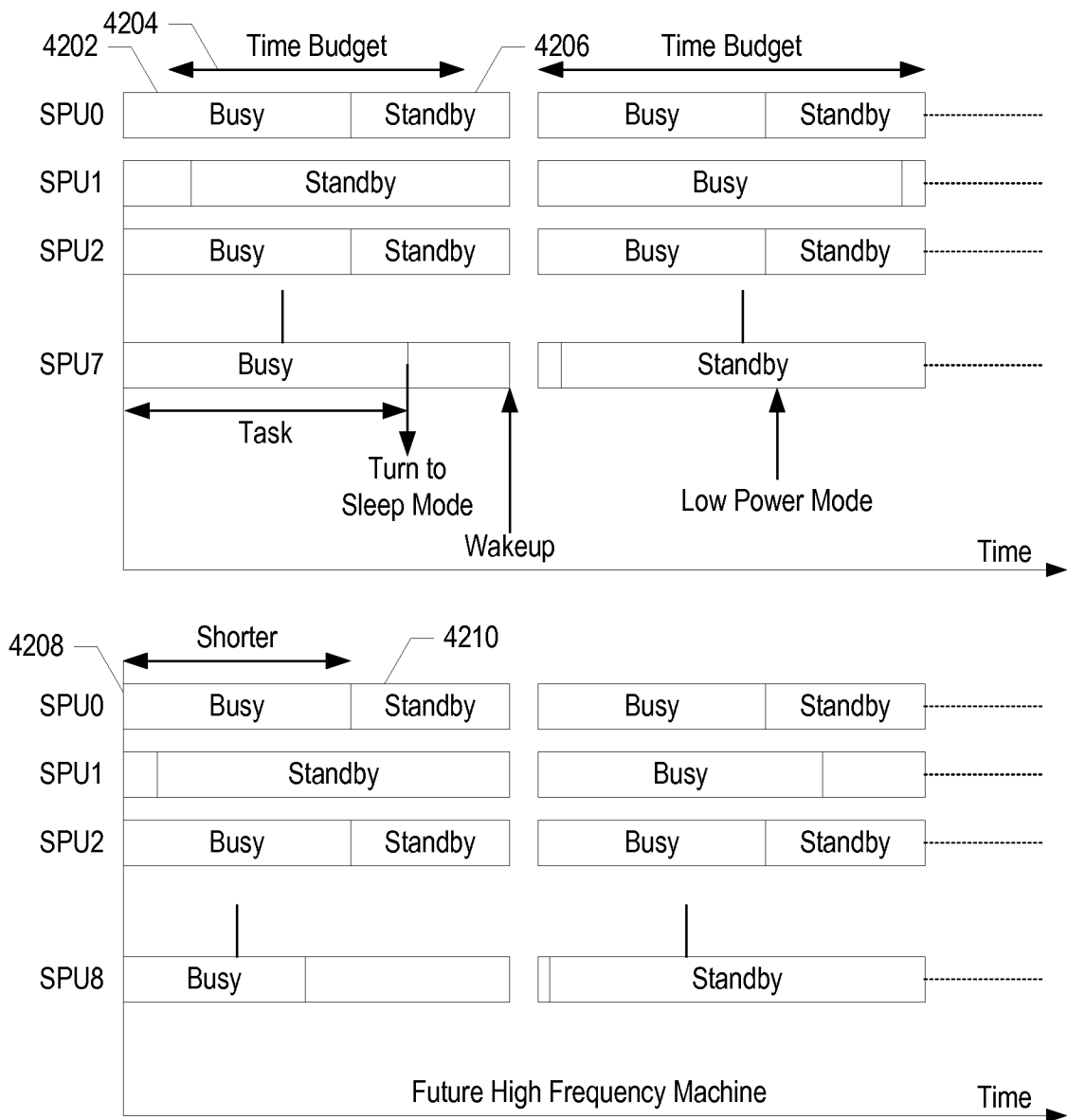


Figure 42

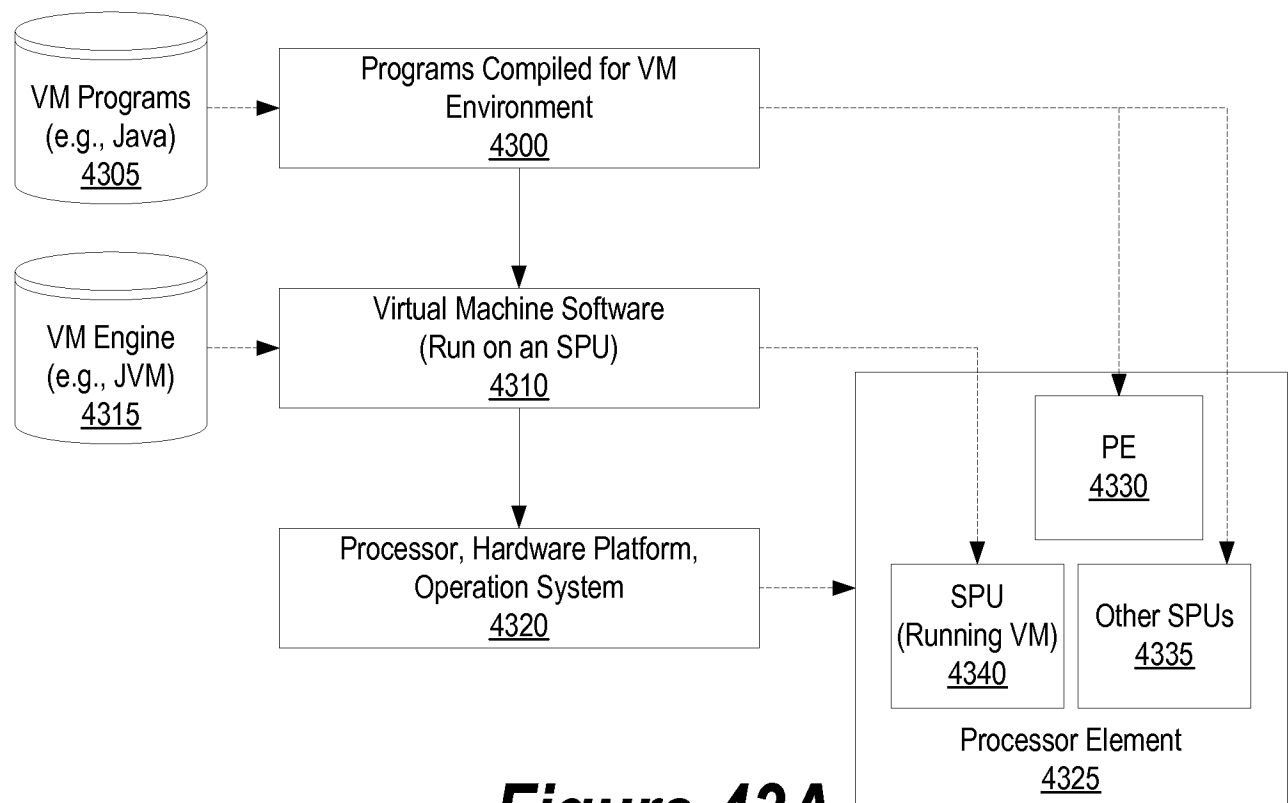


Figure 43A

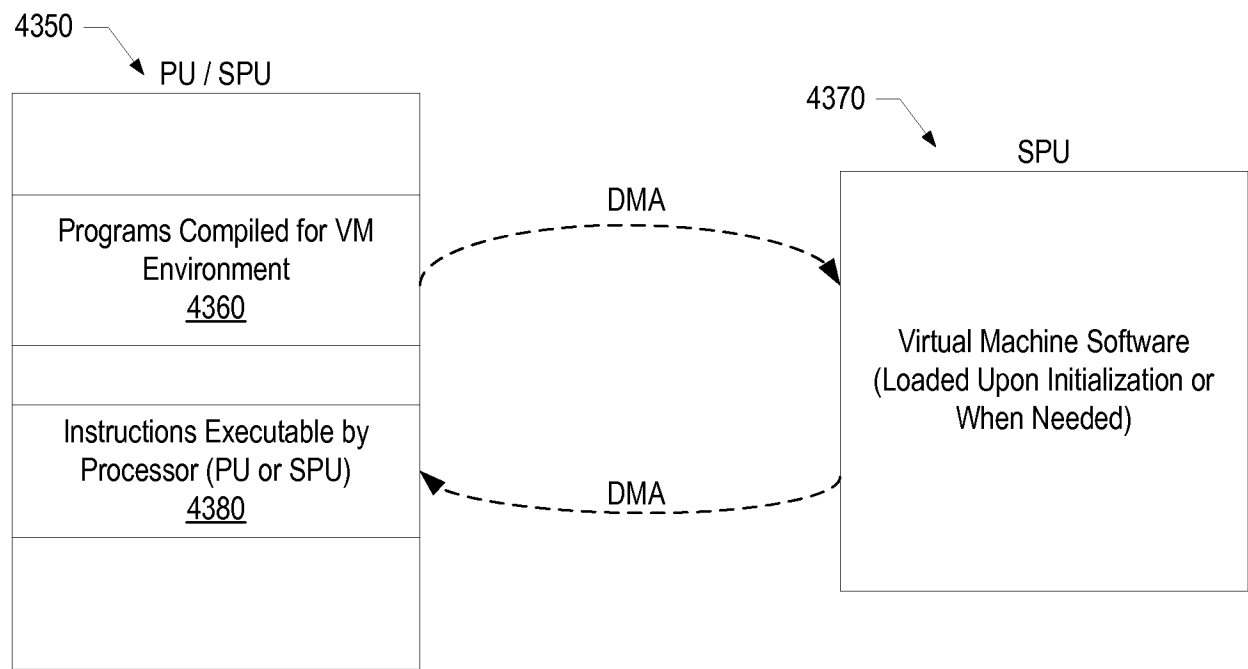
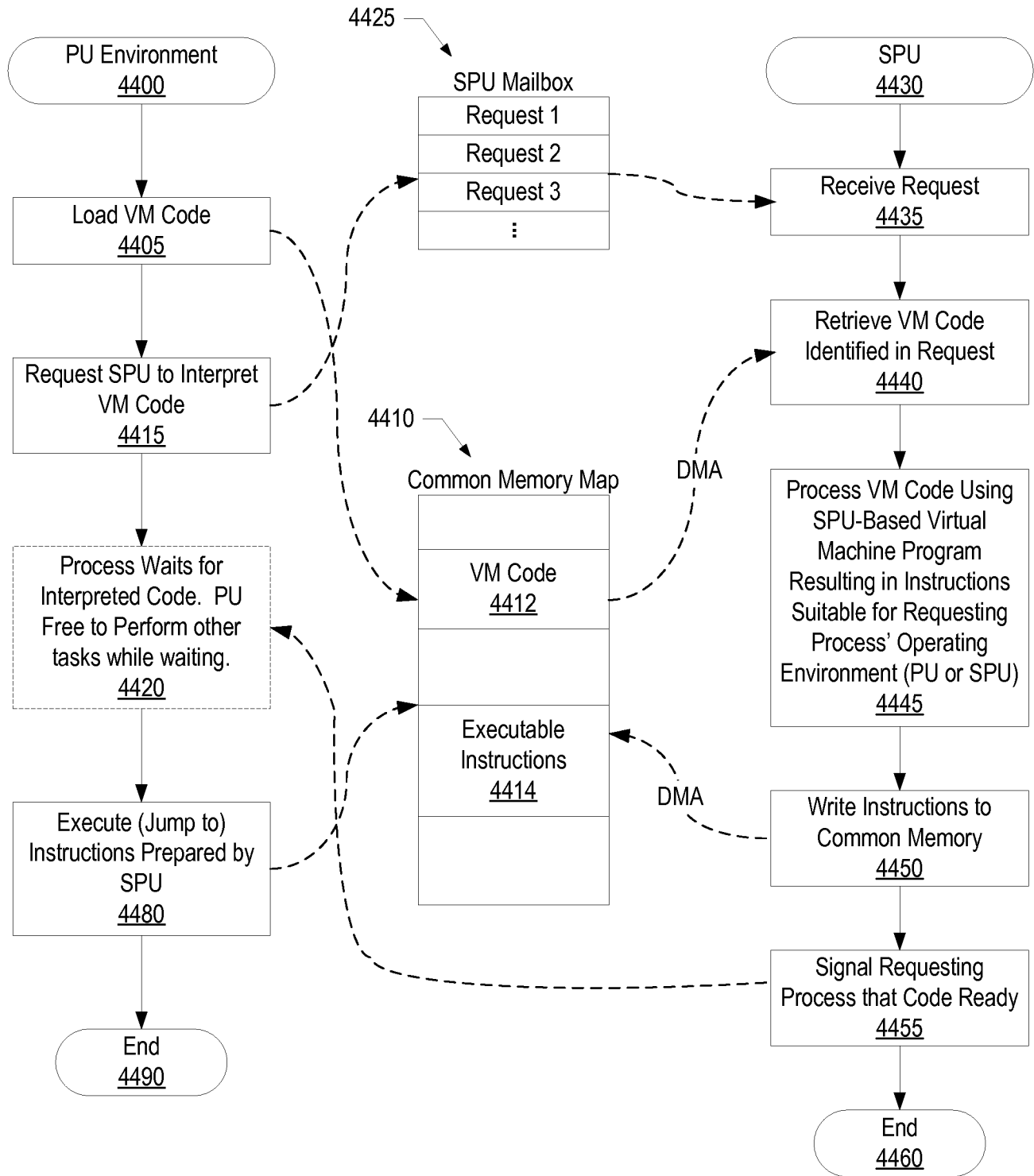
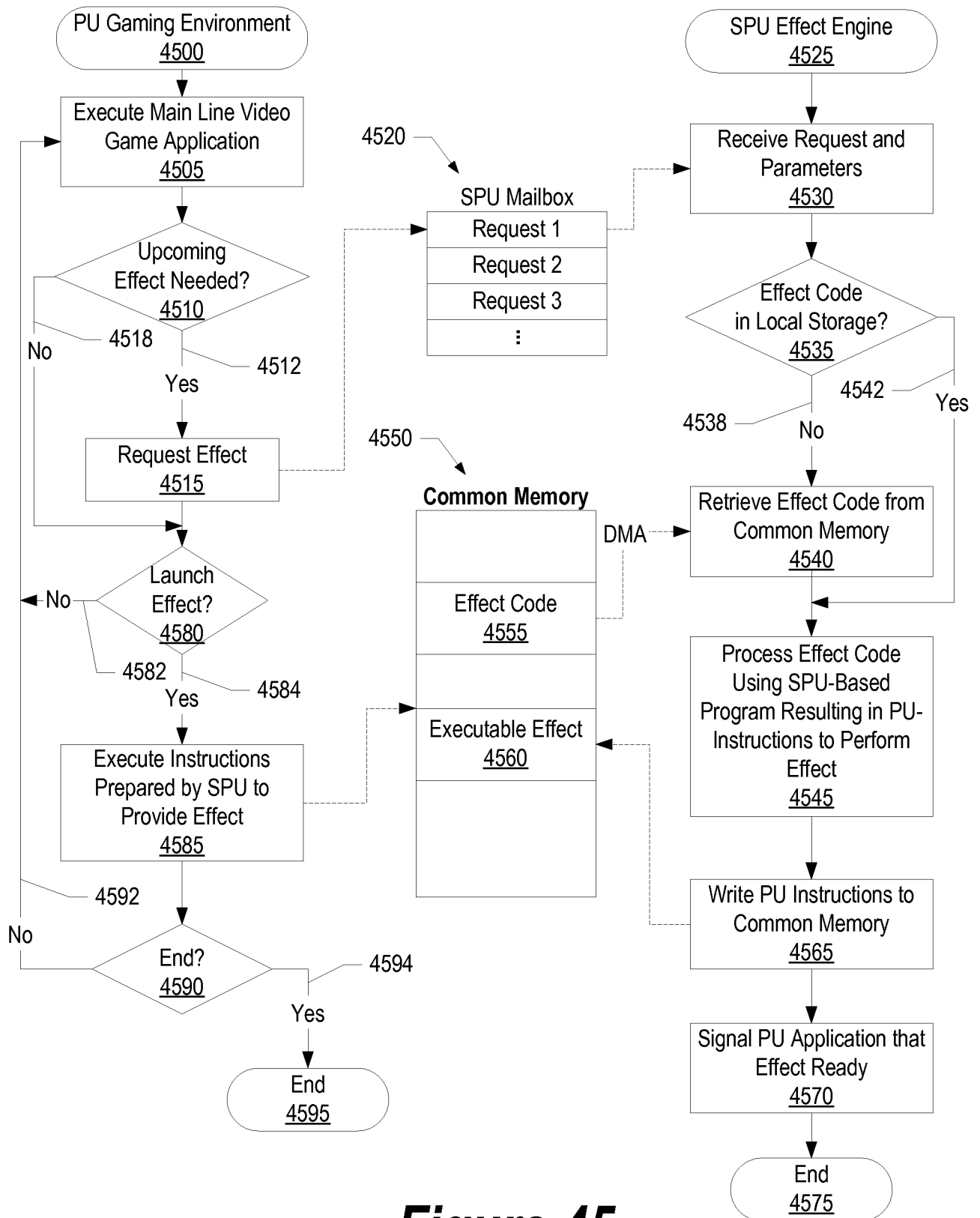


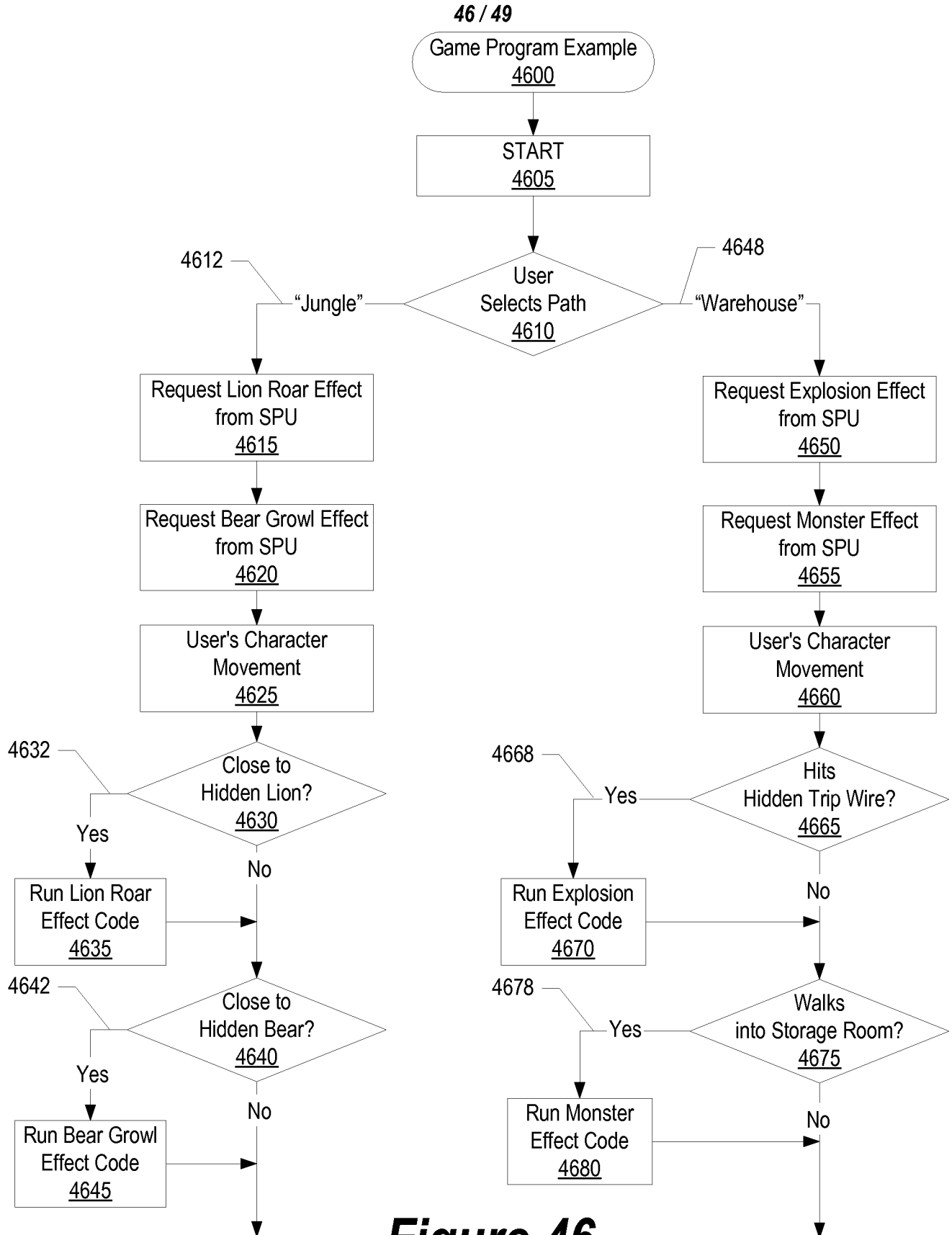
Figure 43B

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**Figure 44**

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**Figure 45**

**Figure 46**

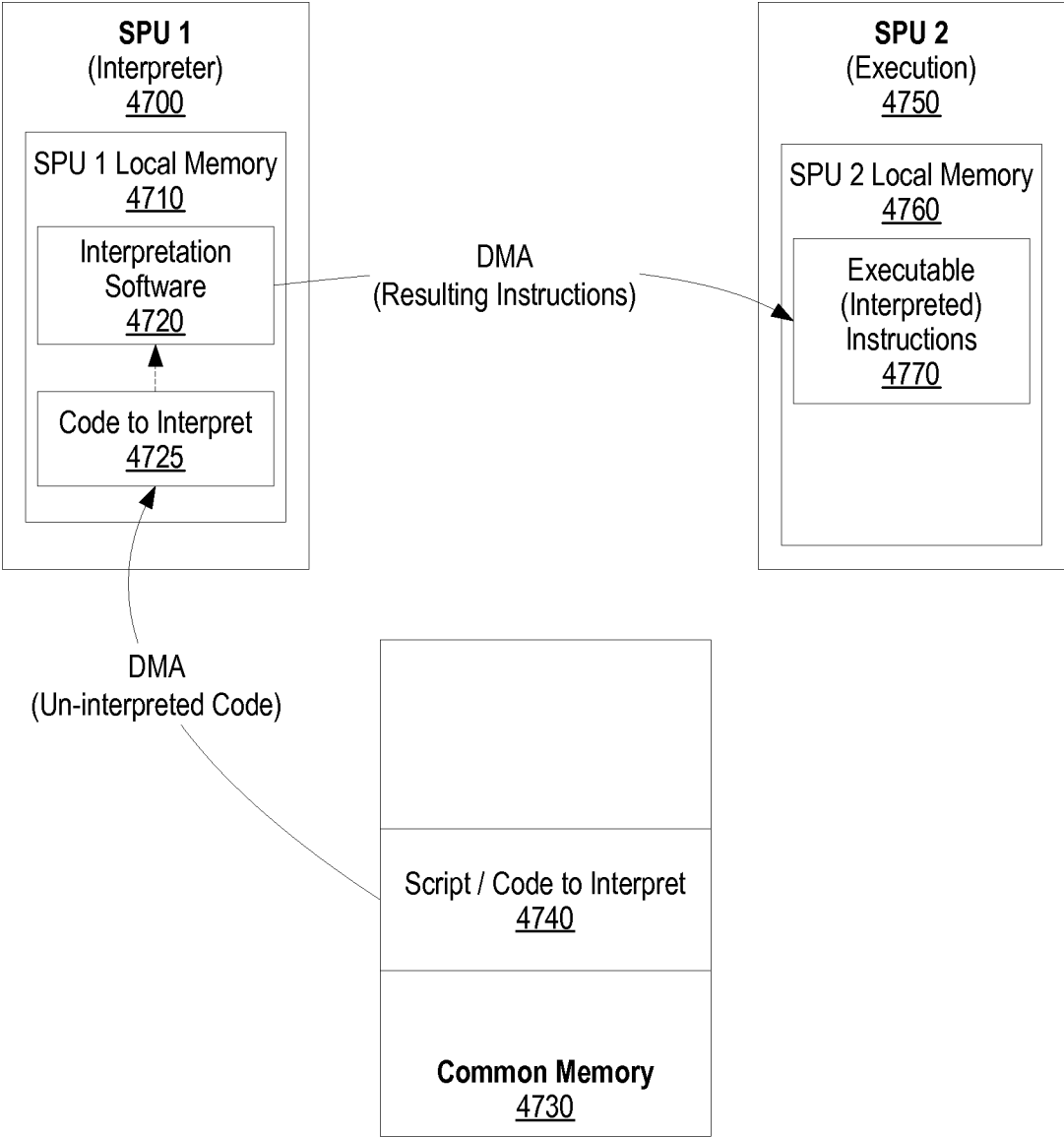
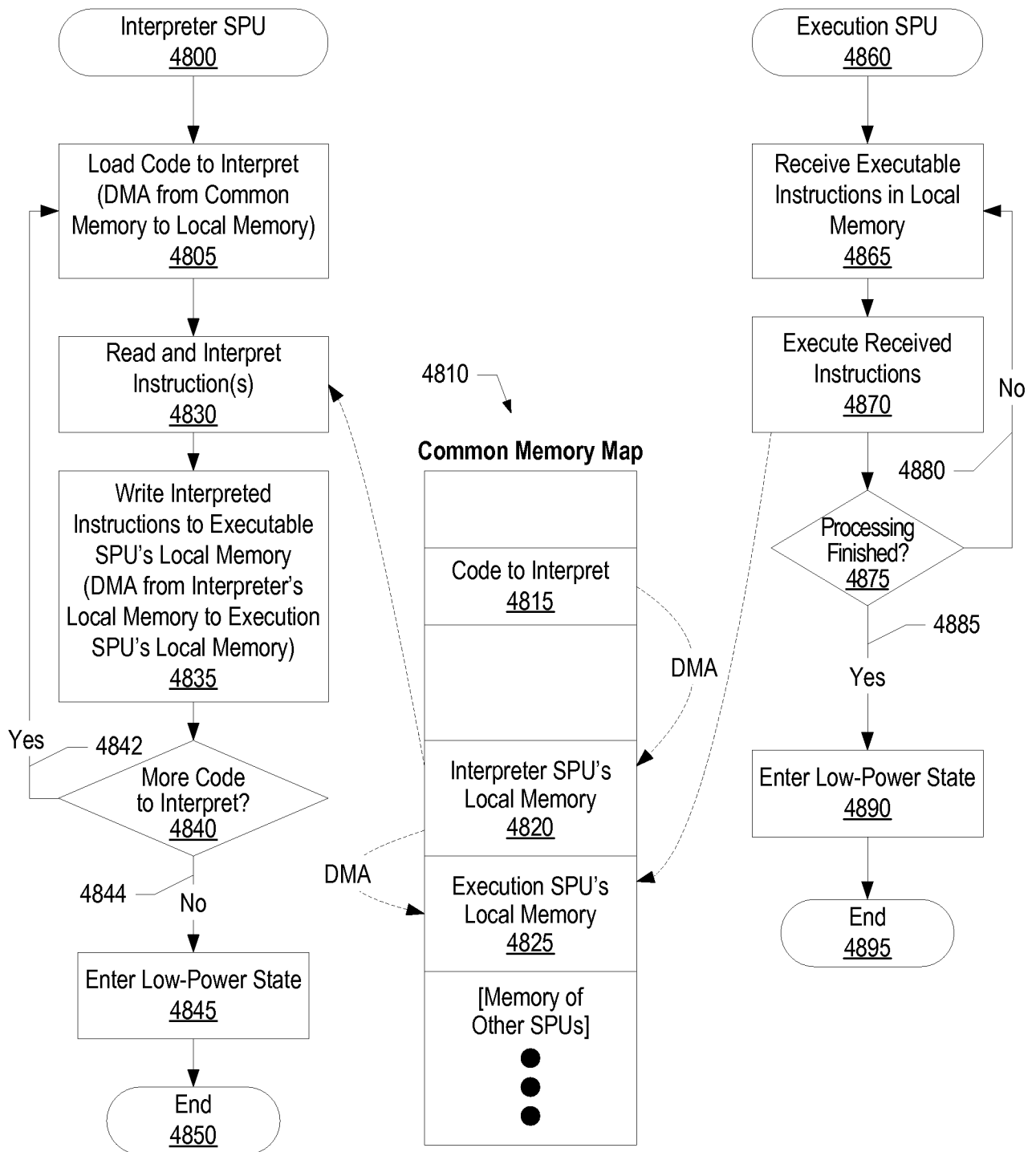


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**Figure 48**

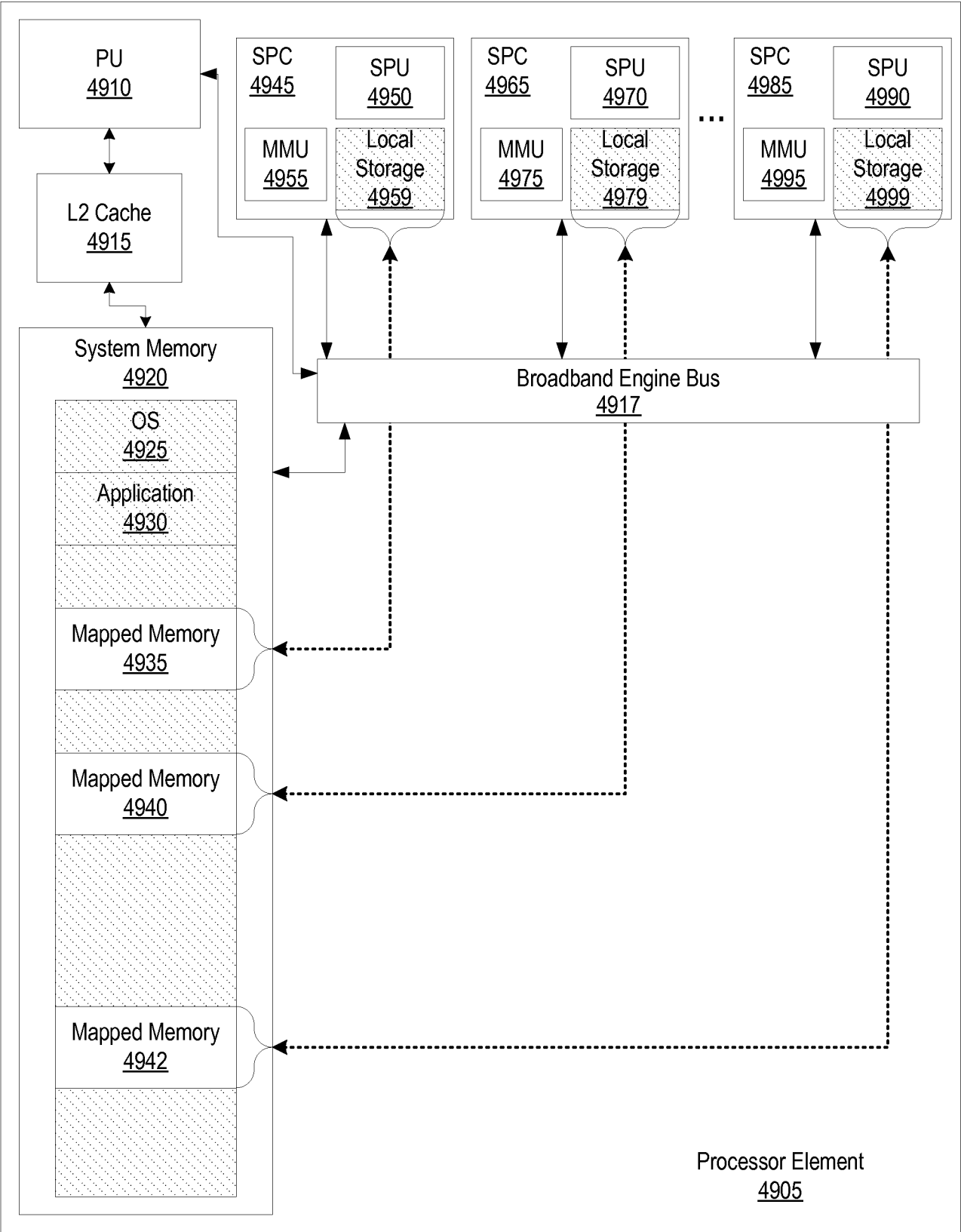


Figure 49